

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	BETA	23-Jan-2003	

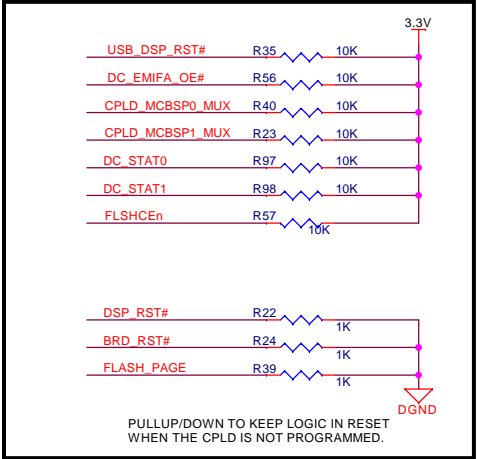
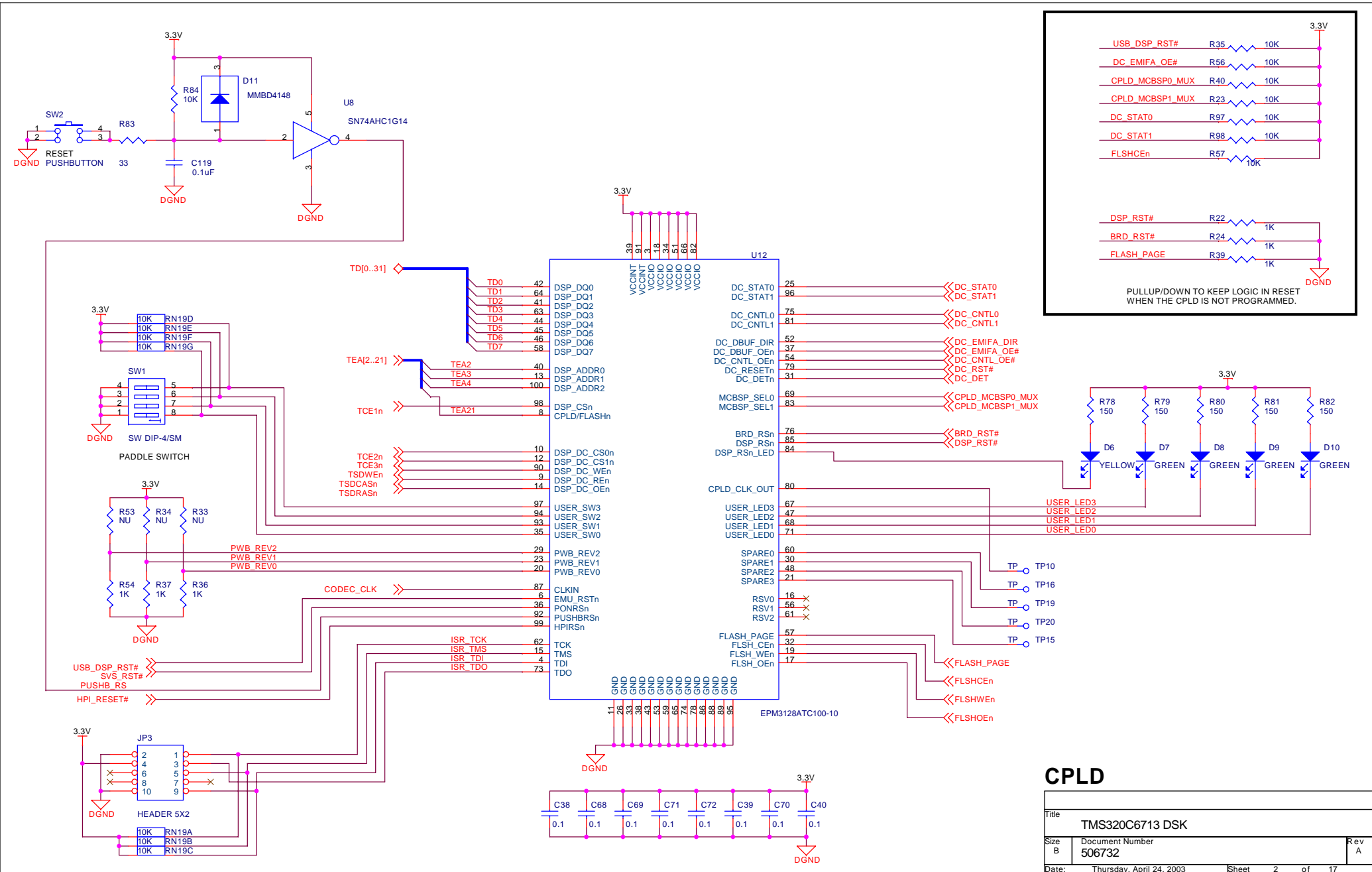
23-April-2003

The TMS320C6713DSK design is based on TMS320C6713 device data sheet SPRS186B and errata SPRZ173E. This schematic is subject to change without notification. Spectrum Digital Inc. assumes no liability for applications assistance, customer product design or infringement of patents described herein.

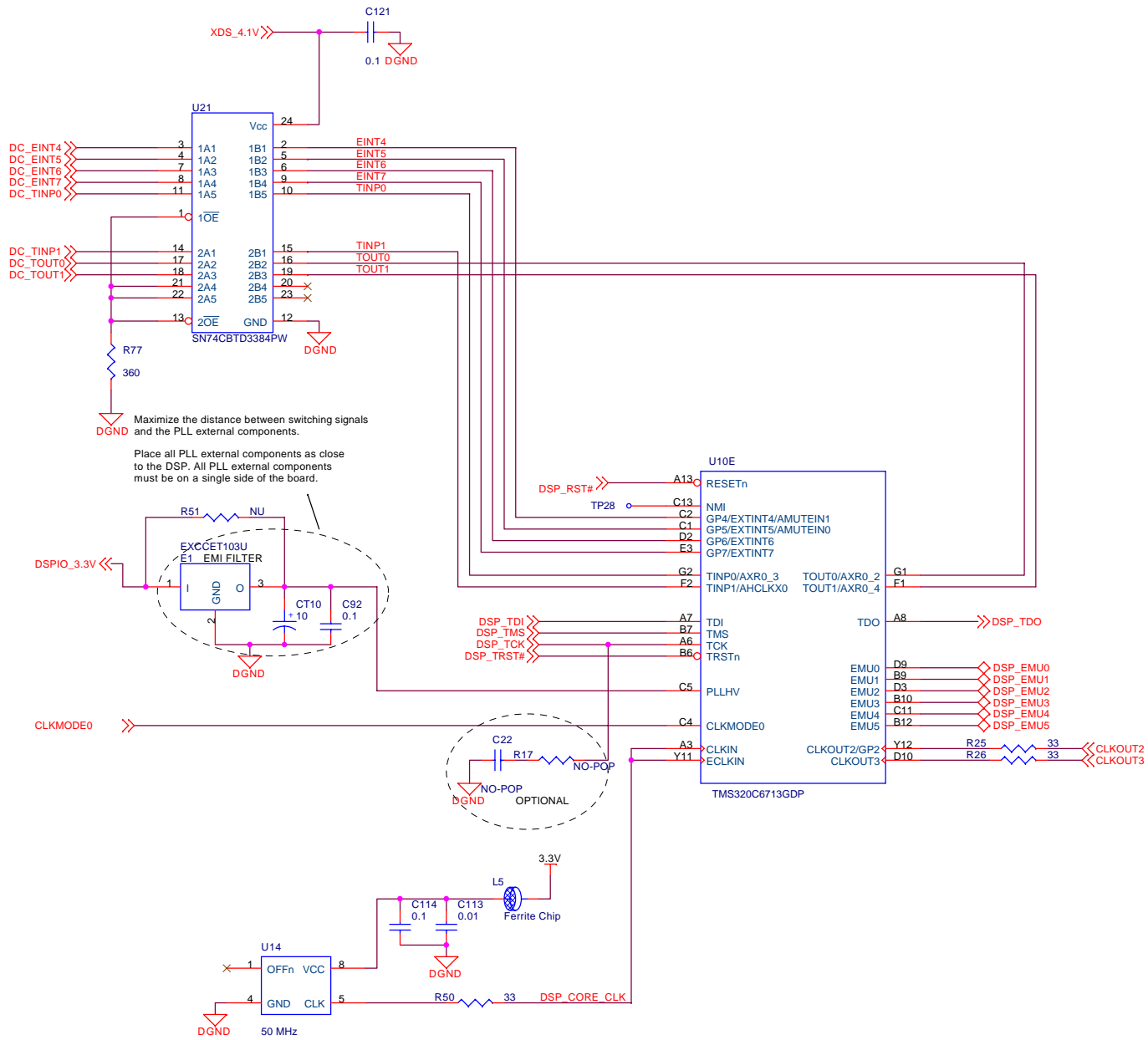
REVISION STATUS OF SHEETS								DWN	DATE
REV								CHK	DATE
SH								ENGR	DATE
REV	A	B	A	A	A	A	A	ENGR-MGR	DATE
SH	8	9	10	11	12	13	14	QA	DATE
REV	B	A	A	A	A	A	B	NEXT ASSY	USED ON
SH	1	2	3	4	5	6	7	MFG	DATE
								ELSE	DATE

SPECTRUM DIGITAL INCORPORATED

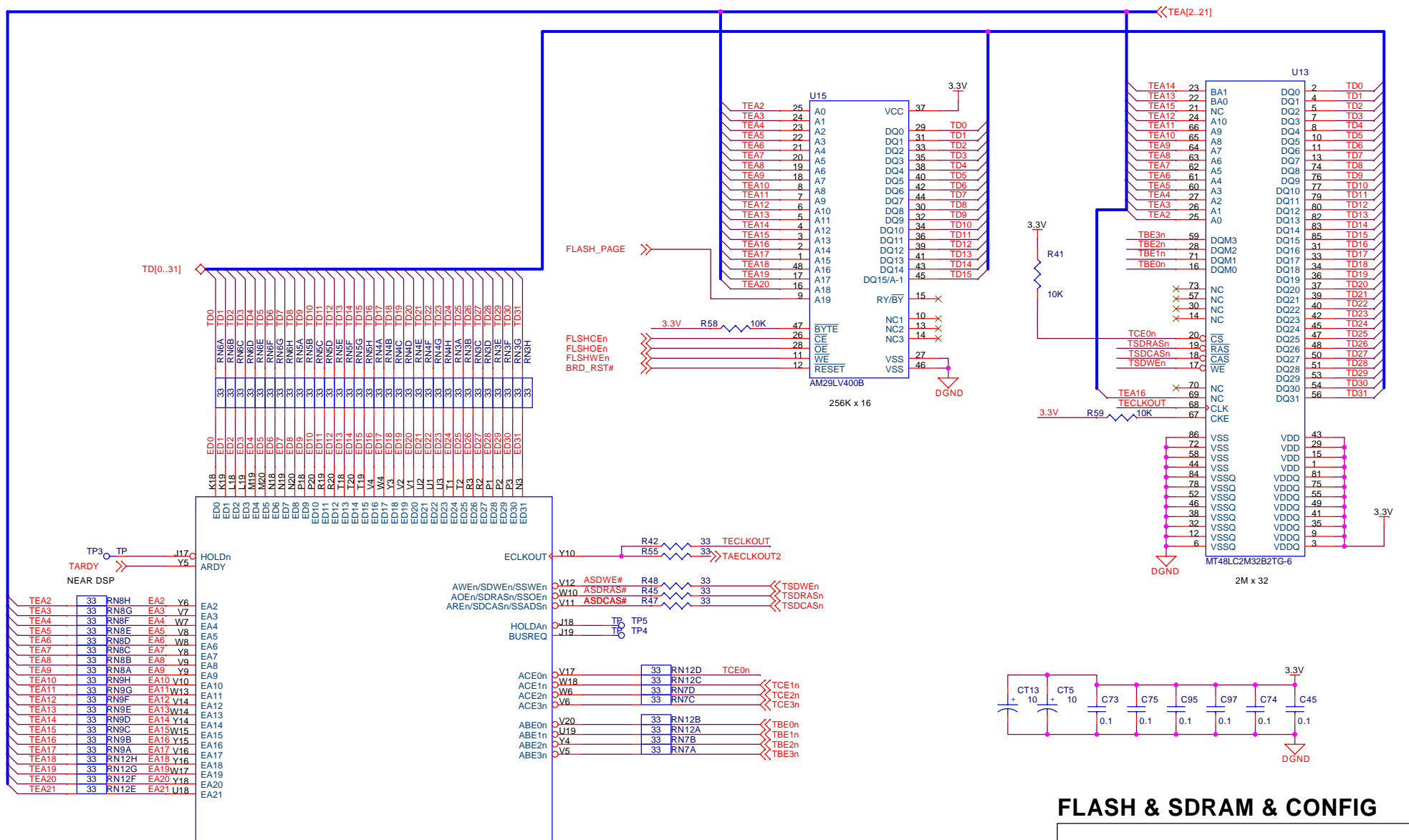
Title			TMS320C6713 DSK
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CPLD		
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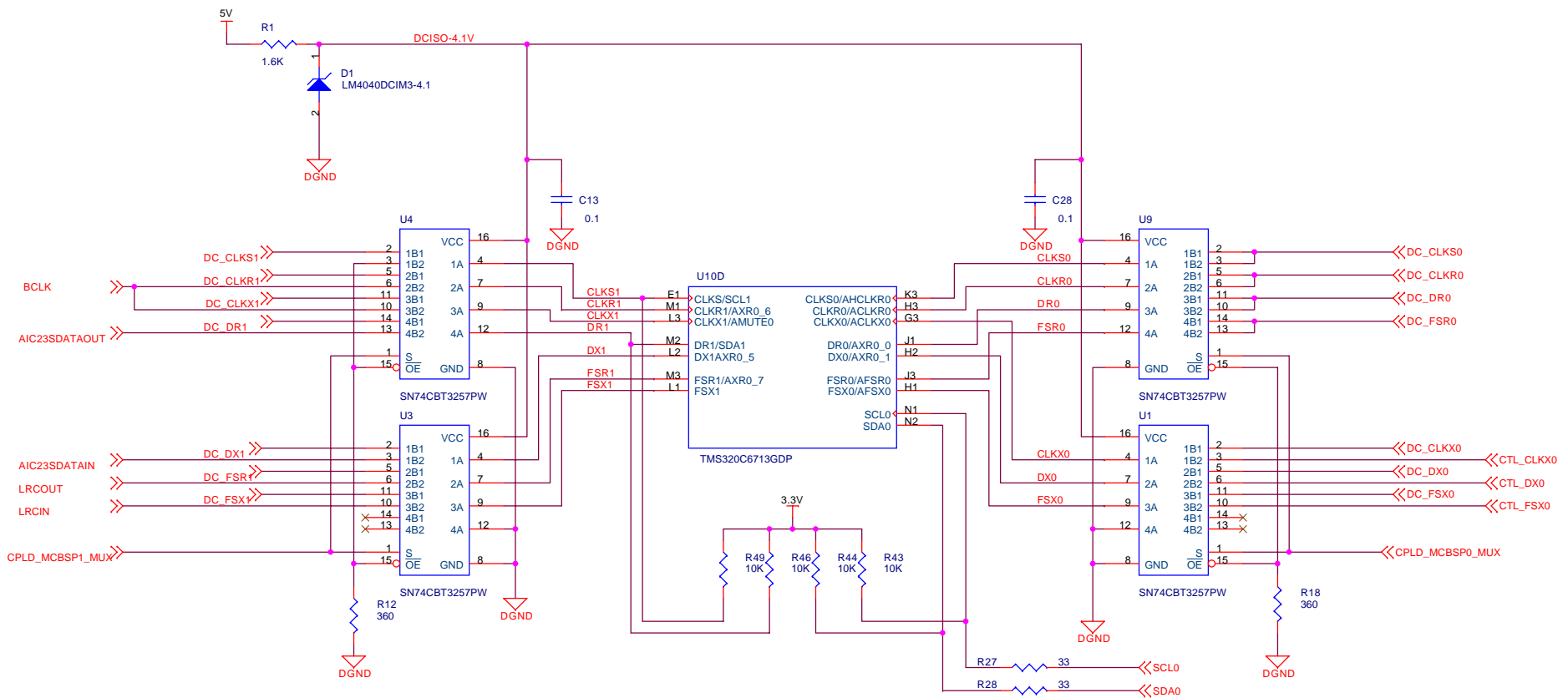


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FLASH & SDRAM & CONFIG

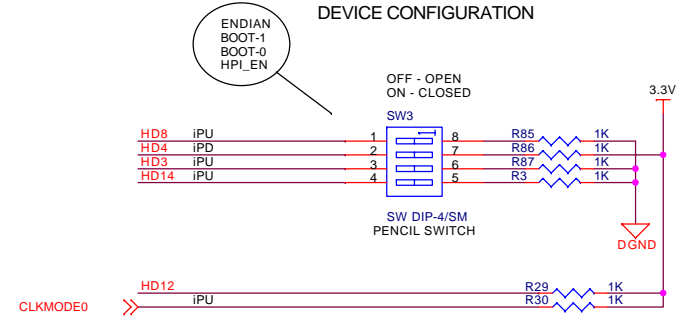
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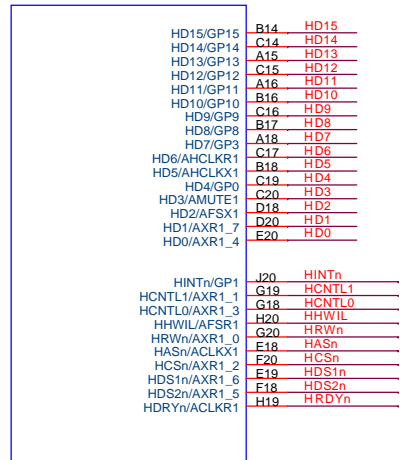
MCBSP

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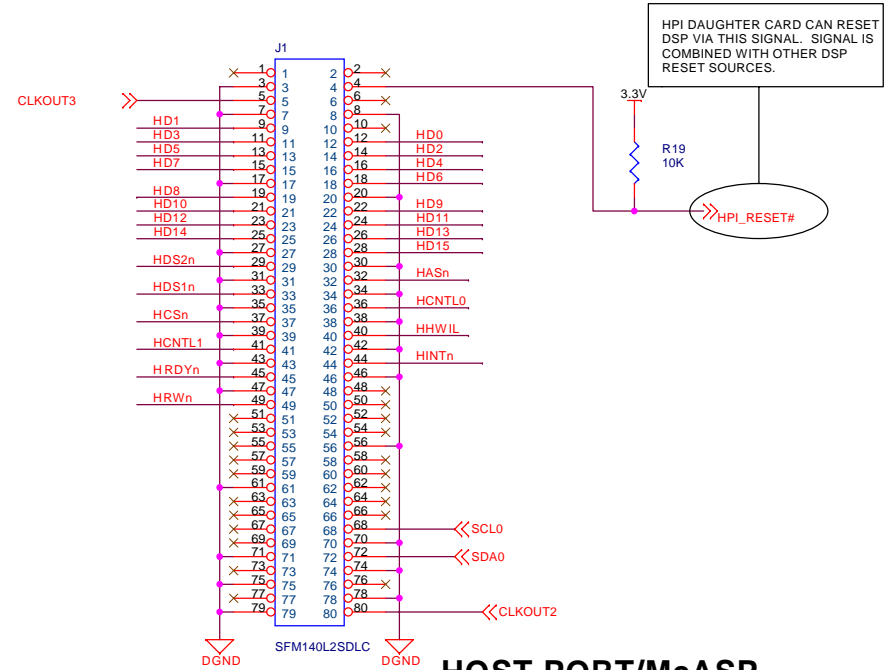
DEVICE CONFIGURATION



U10C

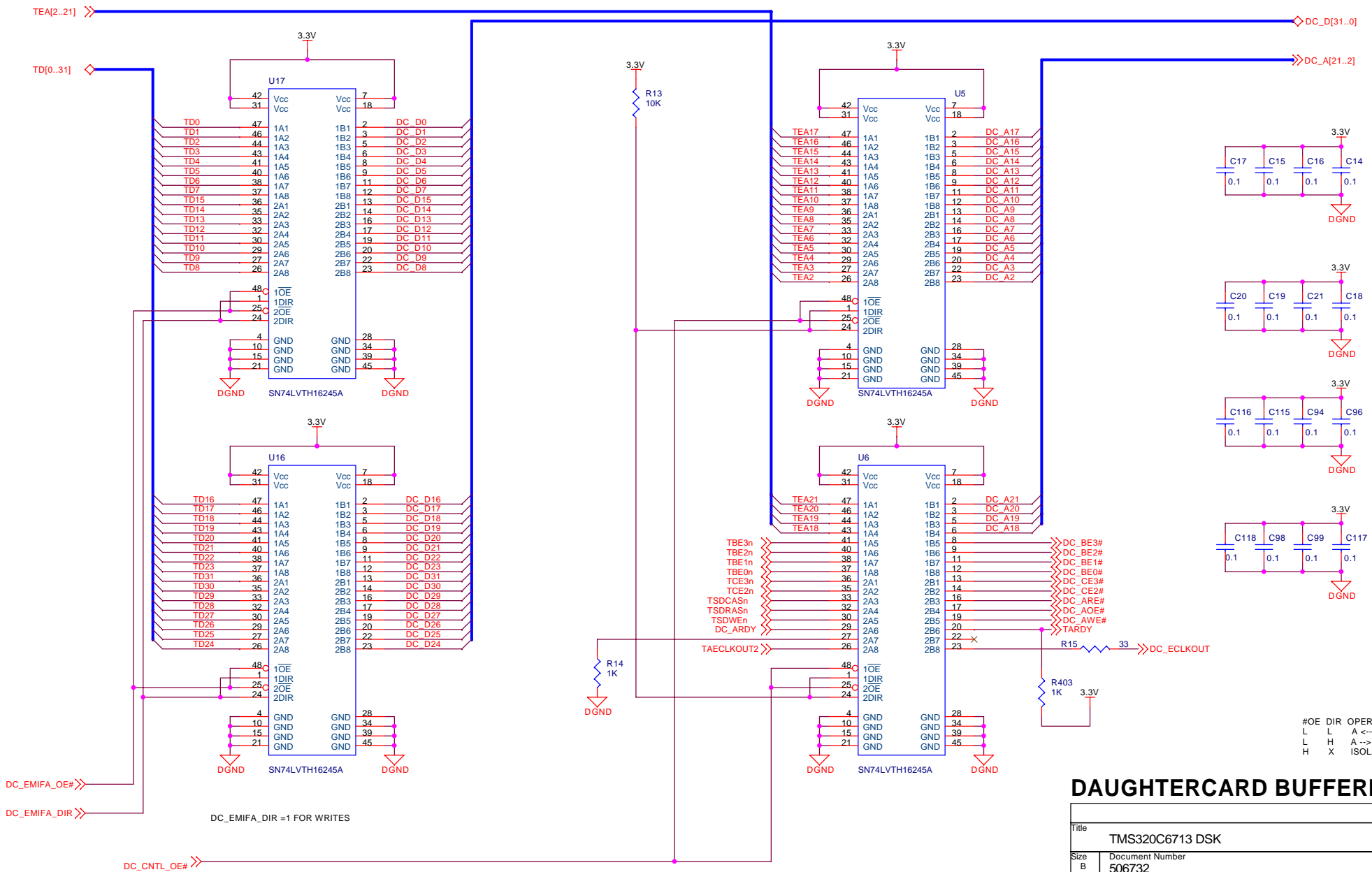


TMS320C6713GDP



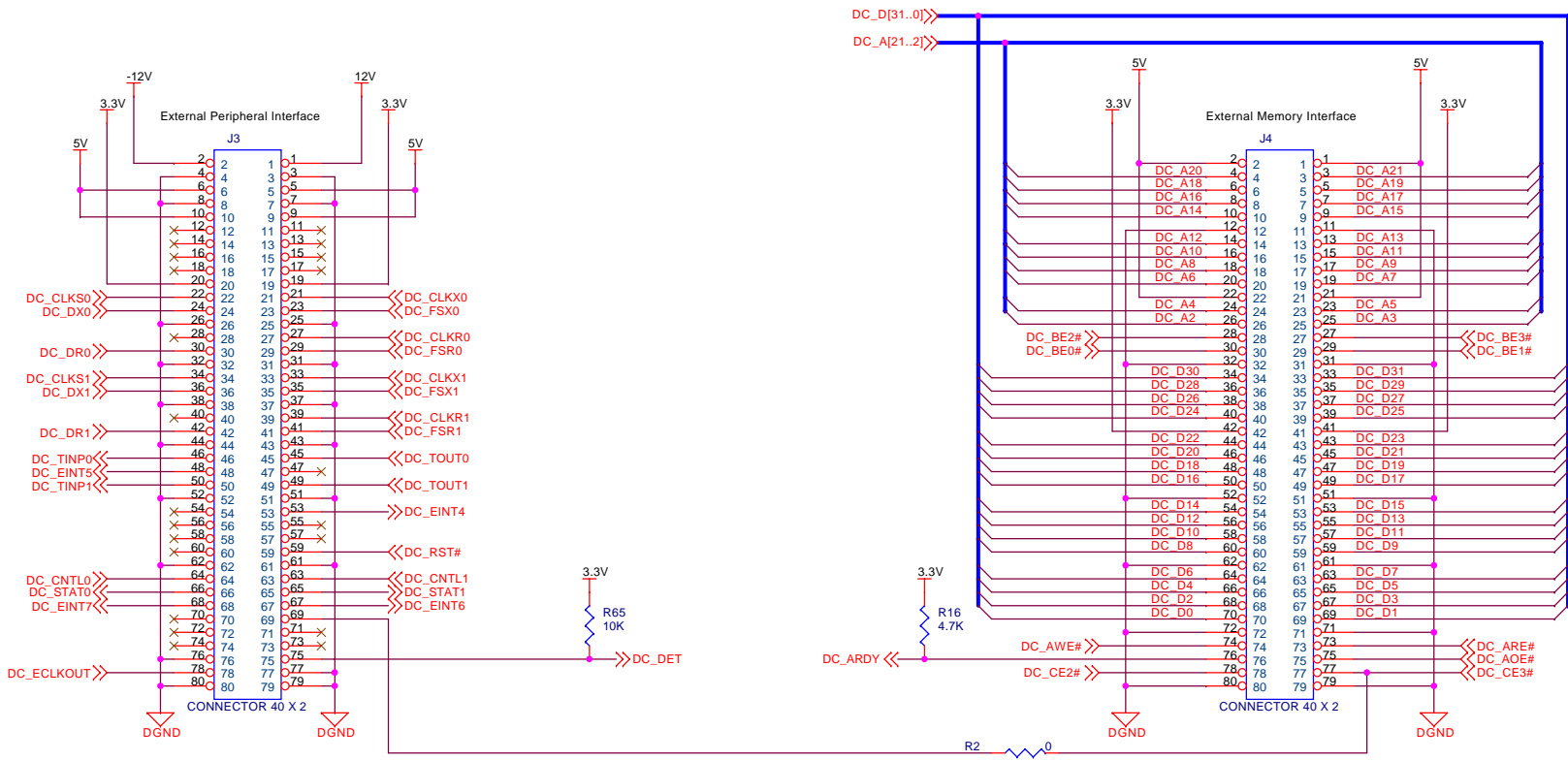
HOST PORT/McASP

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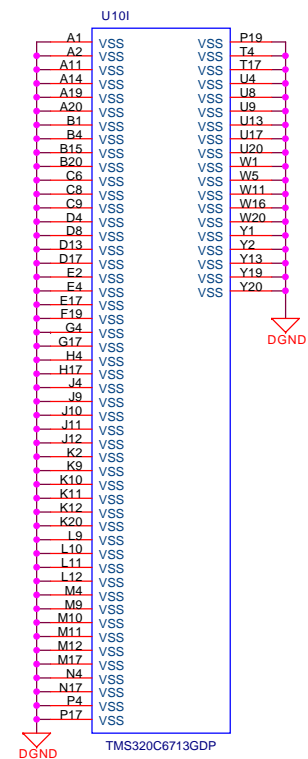
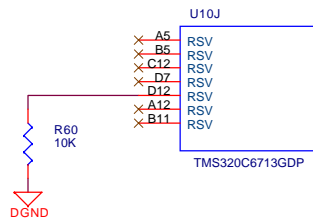
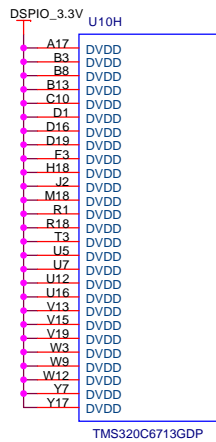
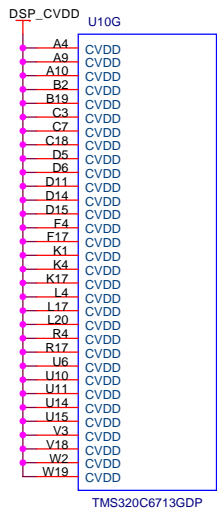
DAUGHTERCARD BUFFERING

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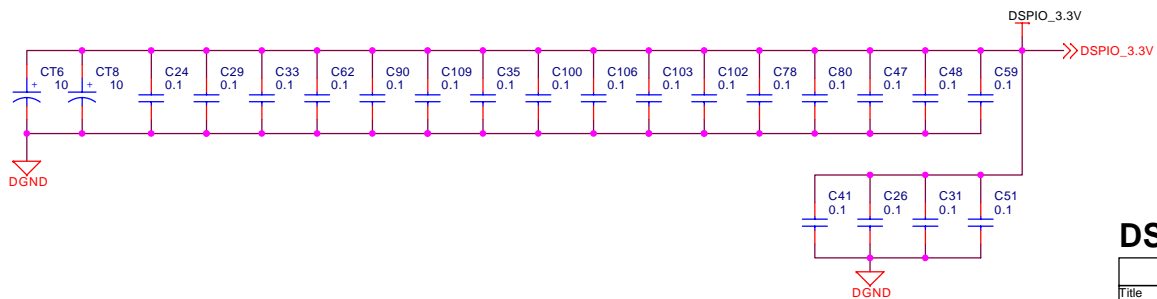
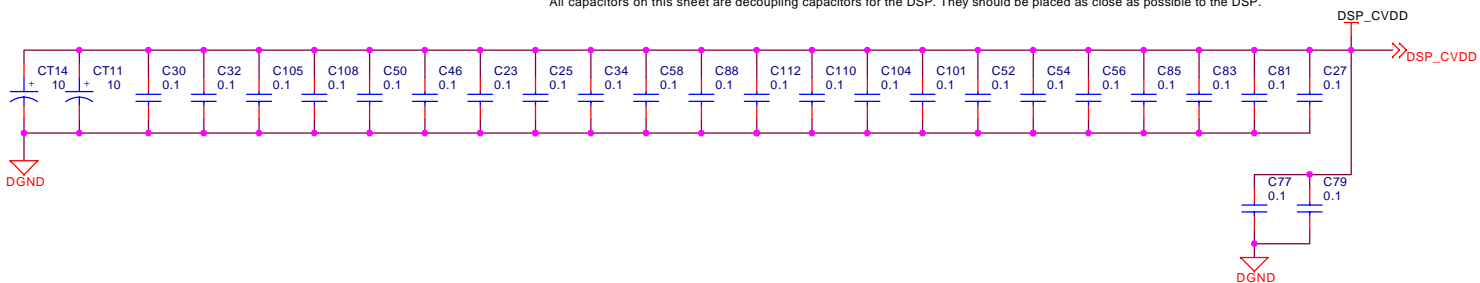


DAUGHTERCARD I/F

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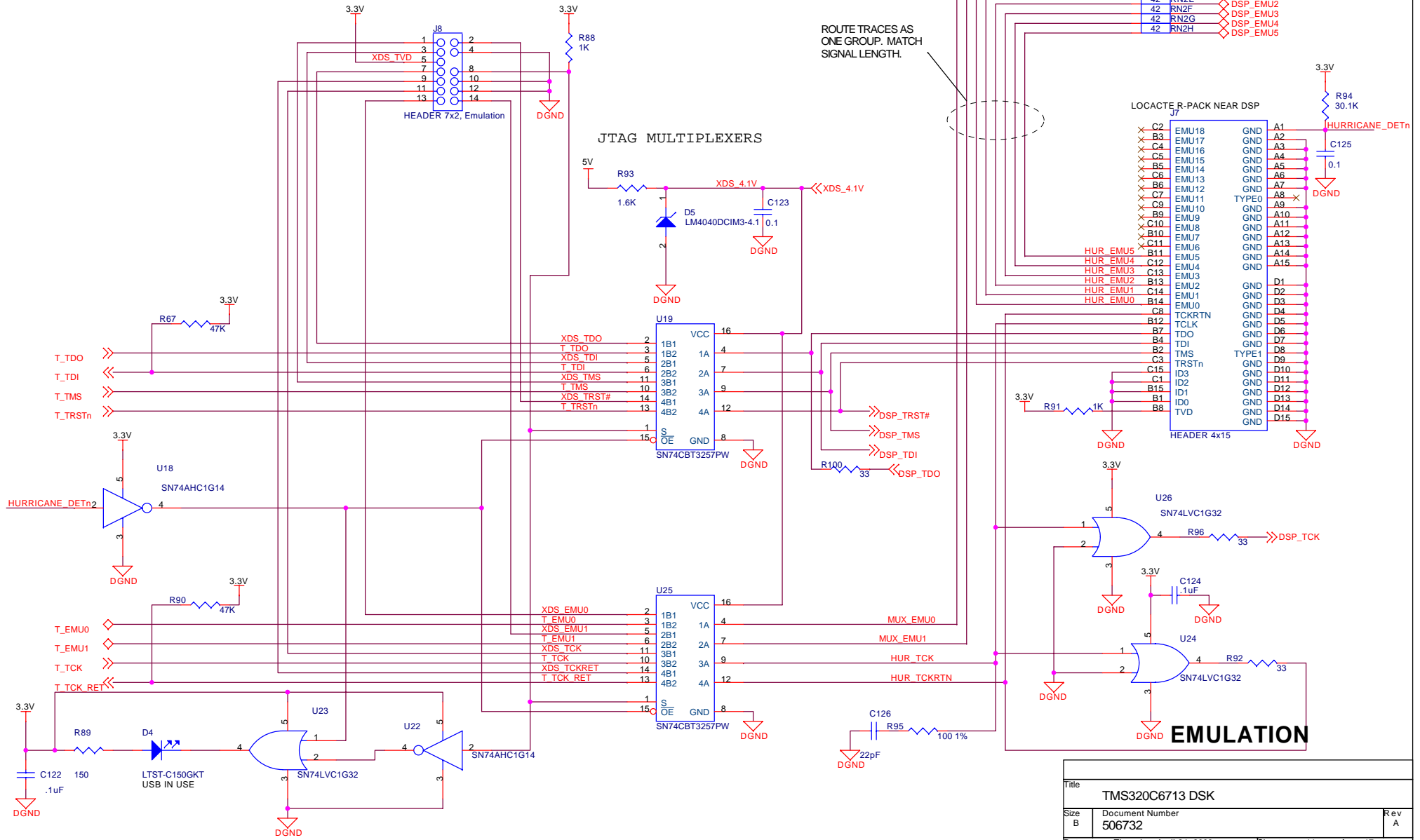
All capacitors on this sheet are decoupling capacitors for the DSP. They should be placed as close as possible to the DSP.



DSP POWER & DECOUPLING

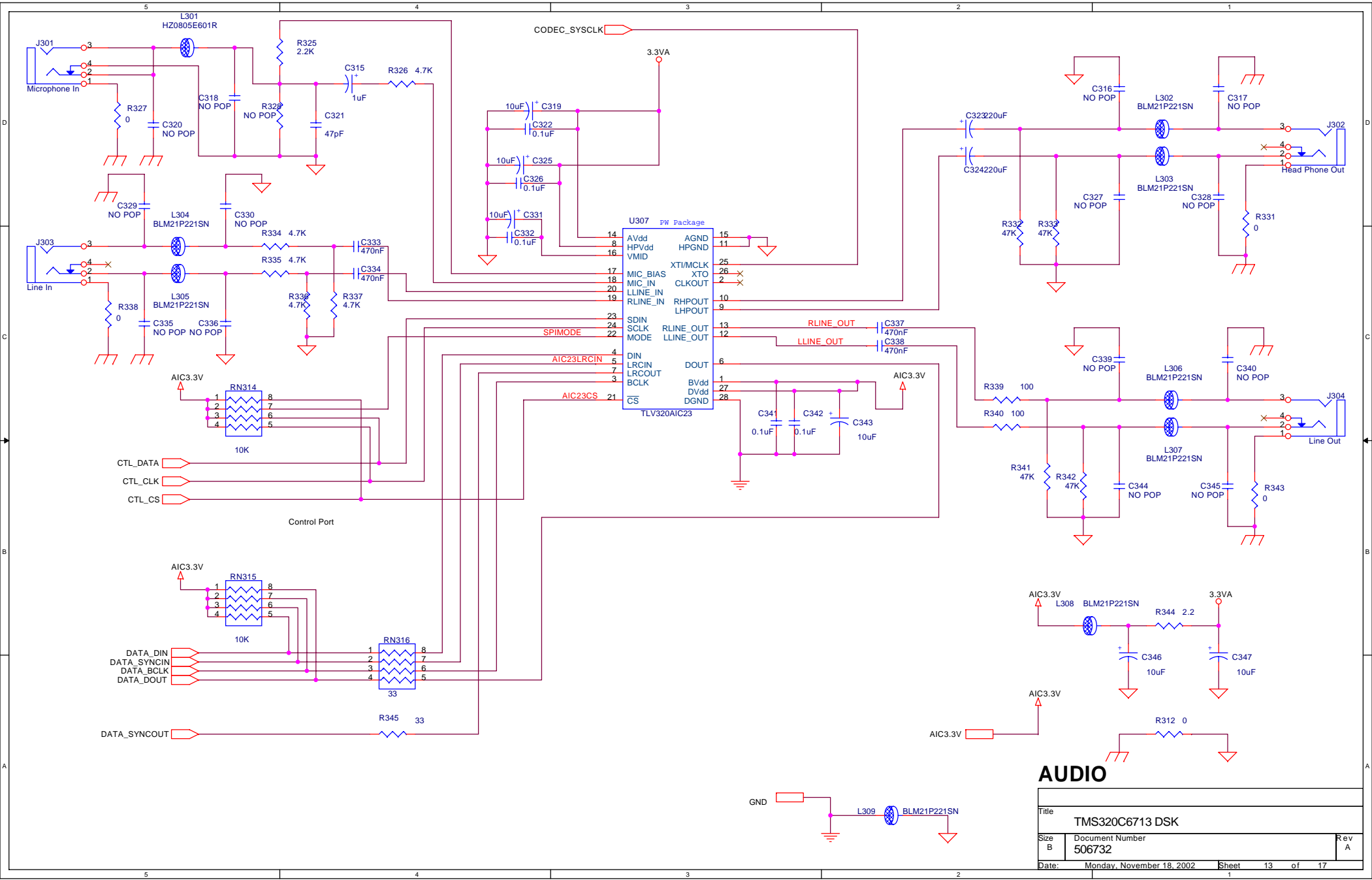
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DSP JTAG HEADER



ROUTE TRACES AS ONE GROUP. MATCH SIGNAL LENGTH.

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AUDIO

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