

SCHEMATIC CONTENTS

SHEET01 - TITLE
 SHEET02 - BLOCK DIAGRAM
 SHEET03 - DSP CLKS/RST/EMU
 SHEET04 - EMULATION
 SHEET05 - DSP Serial/CAN/I2C
 SHEET06 - DSP VP/EMIF/PCI/ENET
 SHEET07 - DSP DDR Interface
 SHEET08 - DSP DACS/GND-pins
 SHEET09 - DSP Power Pins

SHEET10 - Boot DIP Switches
 SHEET11 - VIC
 SHEET12 - I2C Expanders
 SHEET13 - I2C Expanders 2
 SHEET14 - DDR2 Memories
 SHEET15 - EMIF Muxing
 SHEET16 - NAND-Flash/SRAM
 SHEET17 - NOR-Flash/EEPROM
 SHEET18 - ENET Muxing
 SHEET19 - ENET

SHEET20 - CAN
 SHEET21 - RS232
 SHEET22 - PCI-Mux
 SHEET23 - PCI-Mux II
 SHEET24 - PCI-Connector
 SHEET25 - VLYNQ
 SHEET26 - AIC33
 SHEET27 - SPDIF/TVP5146-Switch
 SHEET28 - TVP5146
 SHEET29 - Video Out

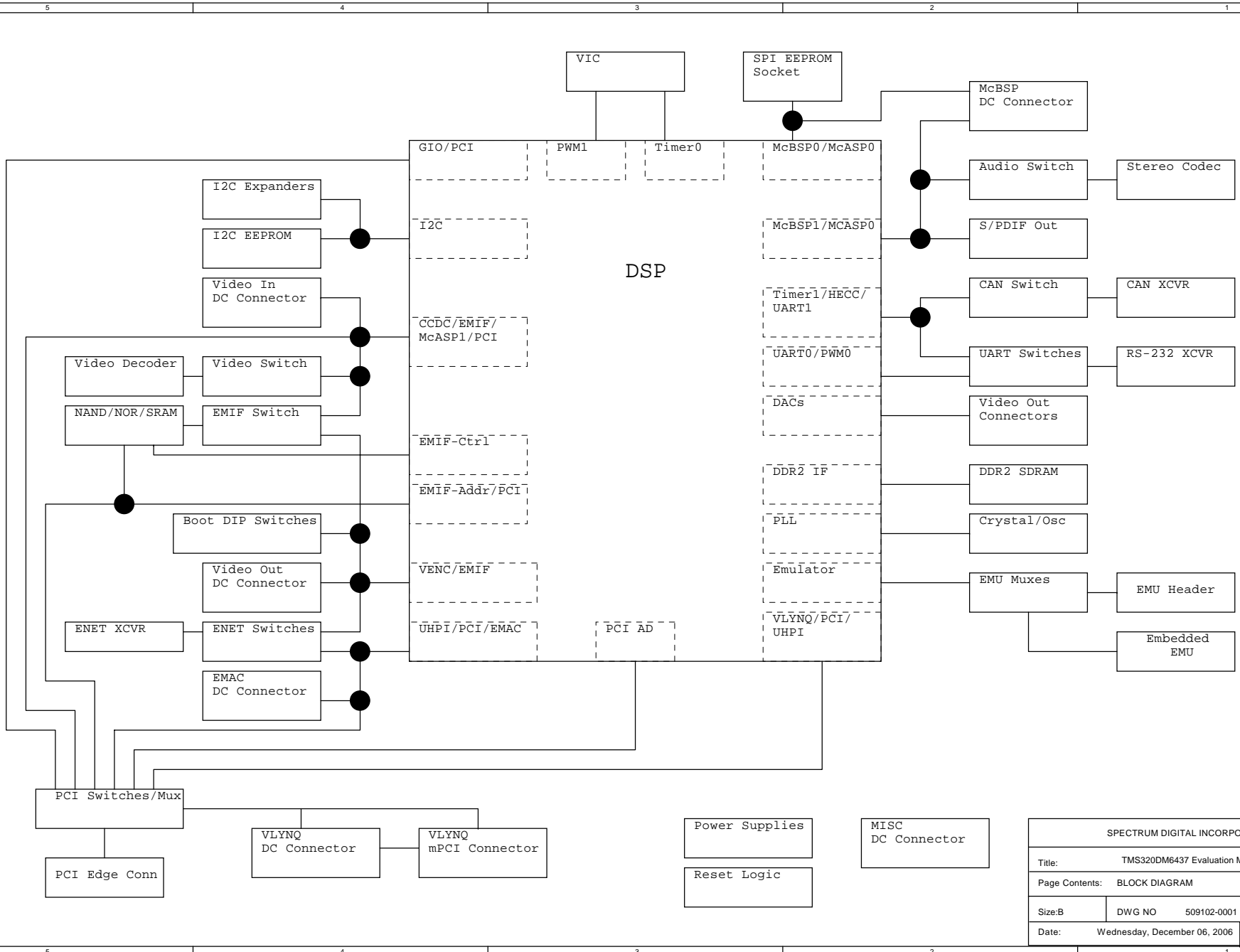
SHEET30 - VIDEO DC Conn.
 SHEET31 - EMAC/MCBSP DC Conn.
 SHEET32 - VLYNQ/EMIF DC Conn.
 SHEET33 - RESET SUPERVISOR
 SHEET34 - POWER

I2C Address Table			
ADDRESS		DEVICE	FUNCTION
HEX	BINARY		
0x38	00111000B	PCF8574	USER INPUT Expander - 0111(A2)(A1)(A0)
0x39	00111001B	PCF8574	LED Expander - 0111(A2)(A1)(A0)
0x3A	00111010B	PCF8574	VIC PLL Expander - 0111(A2)(A1)(A0)
0x3B	00111011B	PCF8574	VLYNQ IO Expander - 0111(A2)(A1)(A0)
0x50	01010000B	CAT24C256	EEPROM - 1010(A2)(A1)(A0)
0x1B	00011011B	AIC33	AUDIO CODEC - 00110(MFP1)(MFP0)
0x5D	01011101B	TVP5146	VIDEO DECODER - 101110(I2CA)

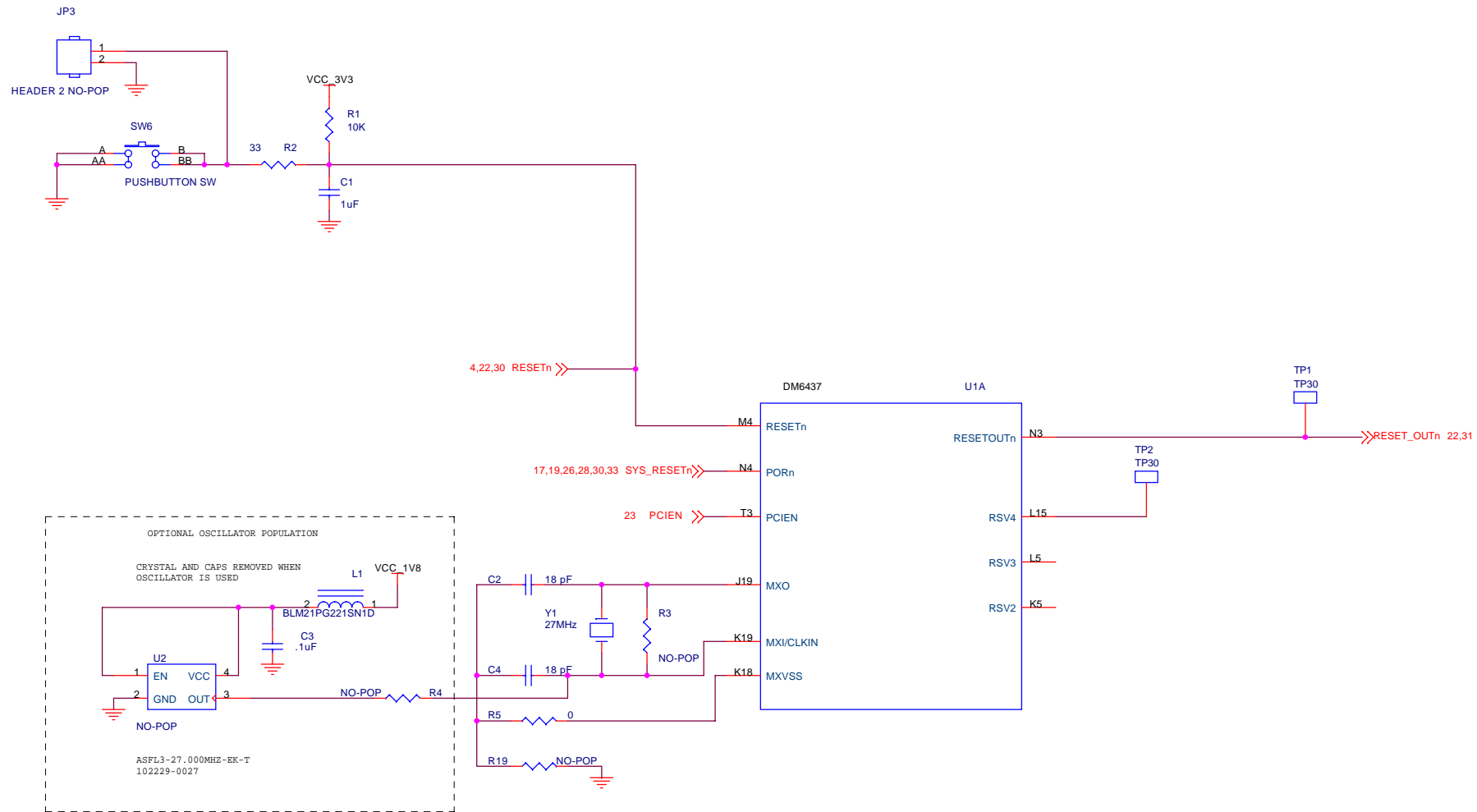
REVISION STATUS OF SHEETS

REV	REVISION STATUS OF SHEETS										APPLICATION	USED ON	DATE	BY		
	A	B	C	D	E	F	G	H	I	J						
	31	32	33	34										DWN	R.R.P.	06/01/2006
														CHK	T.W.K.	06/01/2006
SH	21	22	23	24	25	26	27	28	29	30				ENGR	R.R.P.	06/01/2006
REV	A	C	C	A	A	B	A	A	A	A				ENGR-MGR	R.R.P.	06/01/2006
SH	11	12	13	14	15	16	17	18	19	20				QA	C.M.D.	06/01/2006
REV	C	A	C	A	A	A	A	A	A	A	NEXT ASSY			MFG	R.R.P.	06/01/2006
SH	1	2	3	4	5	6	7	8	9	10	APPLICATION			RLSE	R.R.P.	06/01/2006

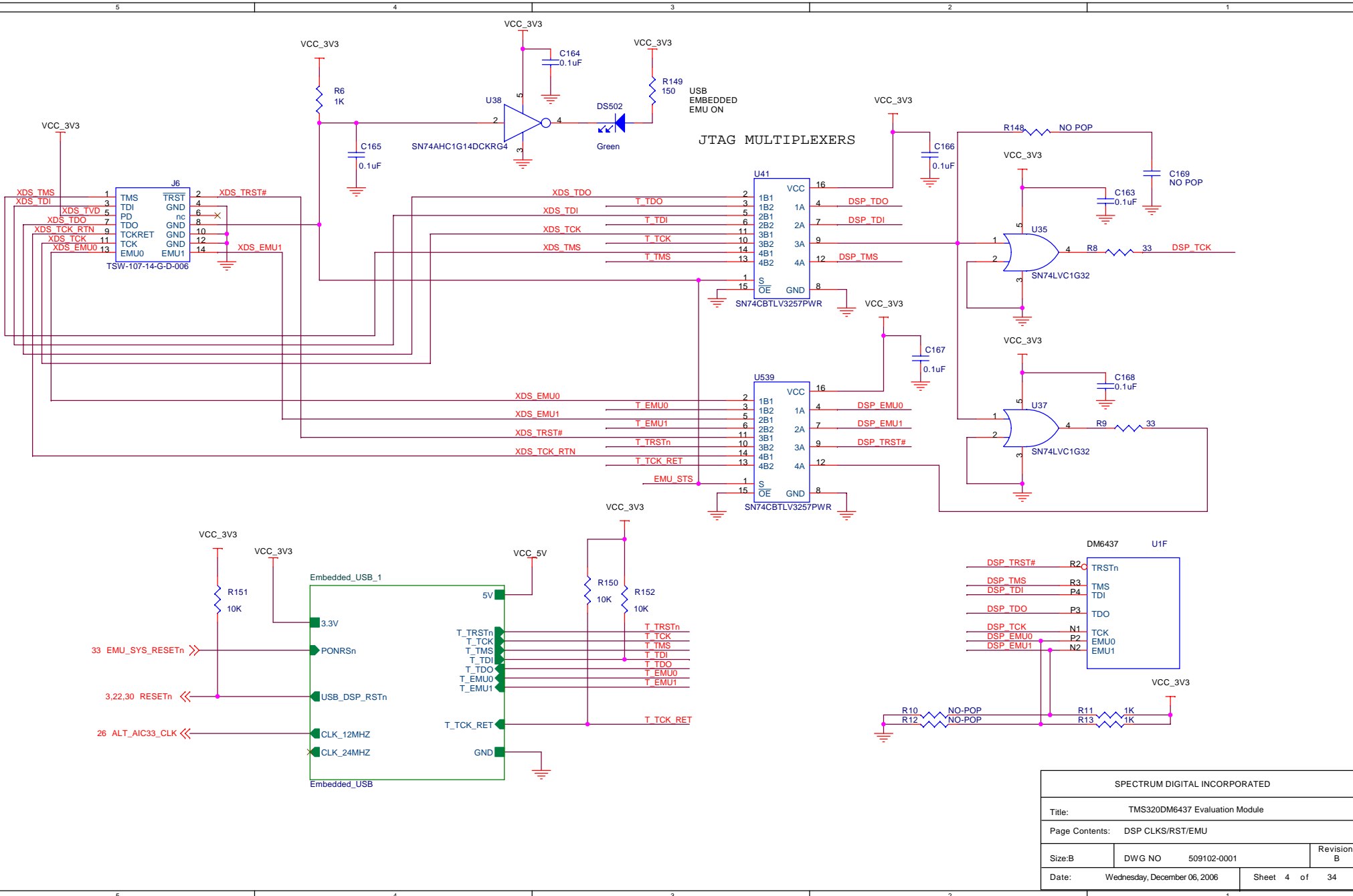
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Size: B	DWG NO	509102-0001	Revision: C
Date: Wednesday, December 06, 2006		Sheet 1 of 34	



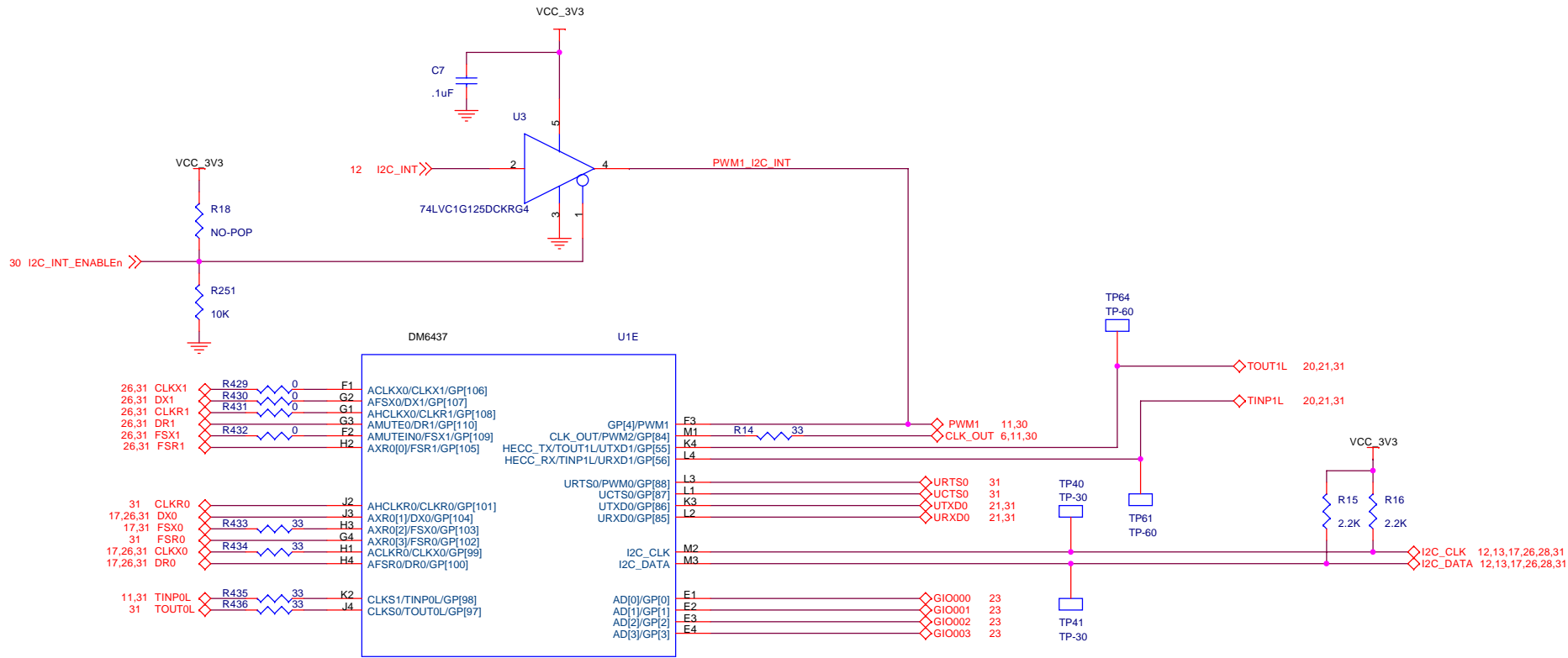
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Date:	Wednesday, December 06, 2006		Sheet 2 of 34



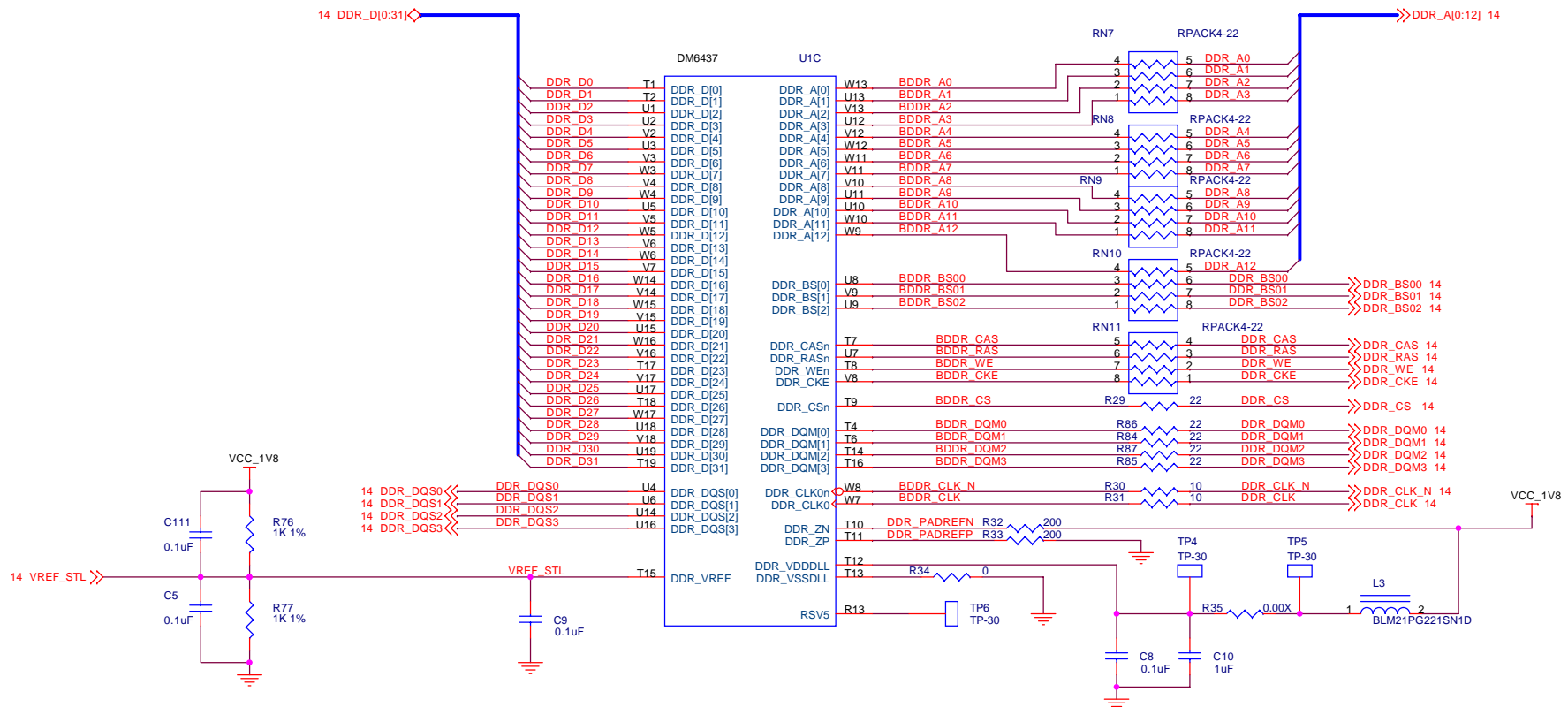
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Size: B	DWG NO	509102-0001	Revision: C
Date:	Wednesday, December 06, 2006	Sheet 3 of	34



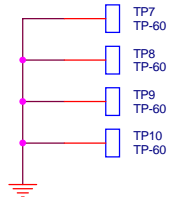
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Size: B	DWG NO	509102-0001	Revision: B
Date:	Wednesday, December 06, 2006	Sheet 4 of	34



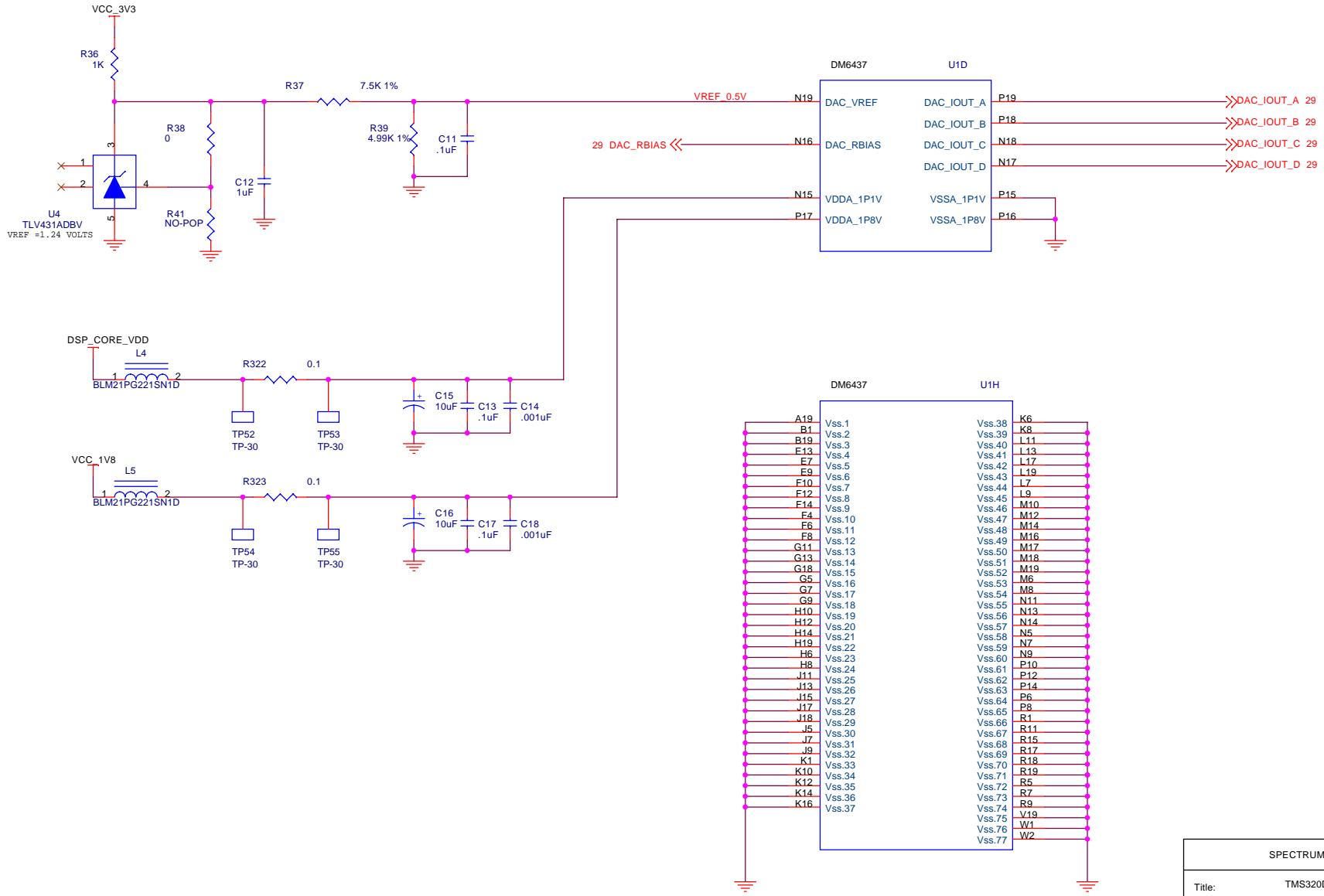
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Date: Wednesday, December 06, 2006	Sheet 5 of 34		



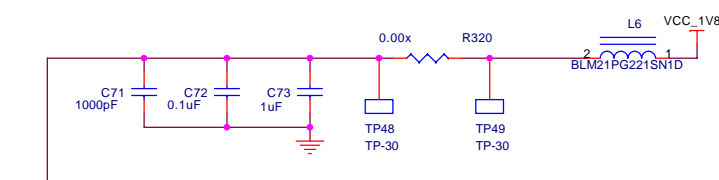
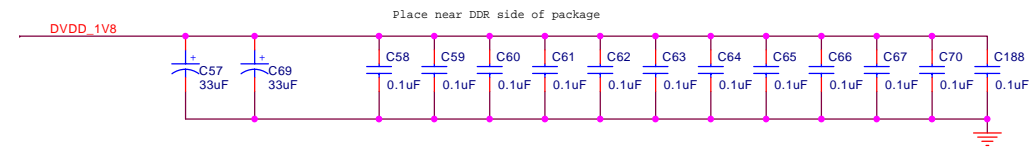
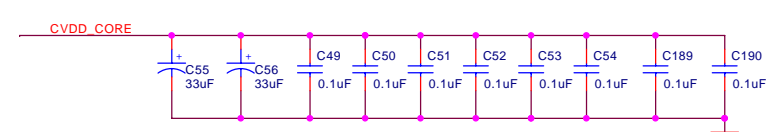
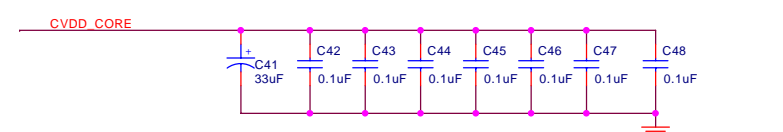
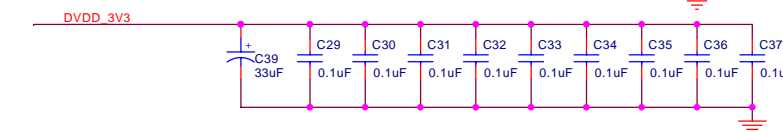
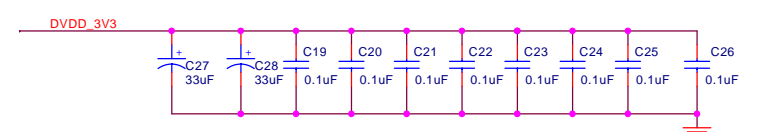
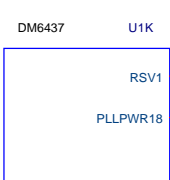
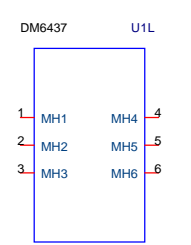
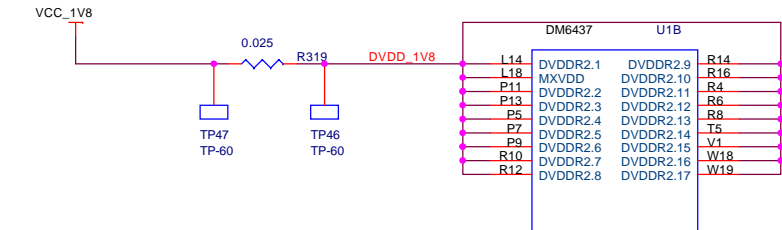
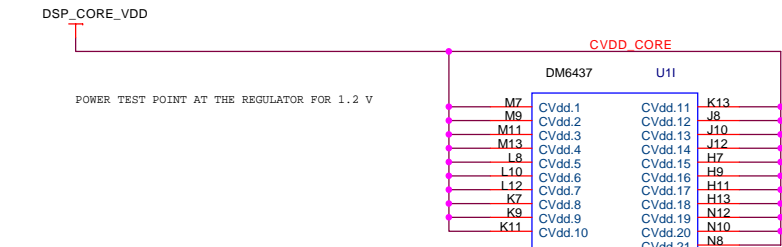
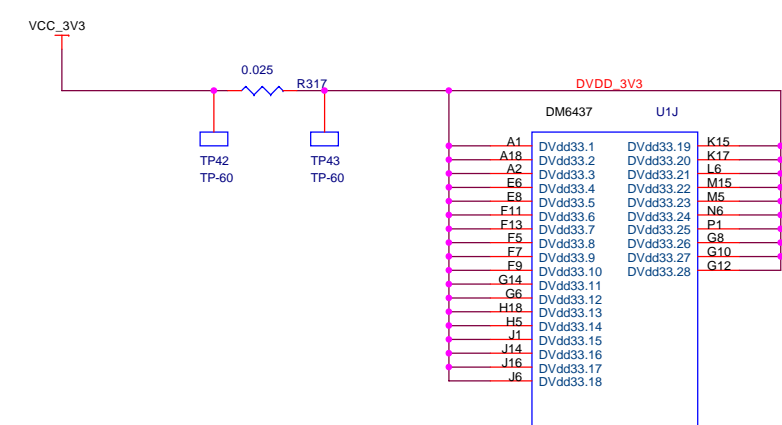
Designers must use routing techniques from
 DDR2 PCB
 Layout Application Note



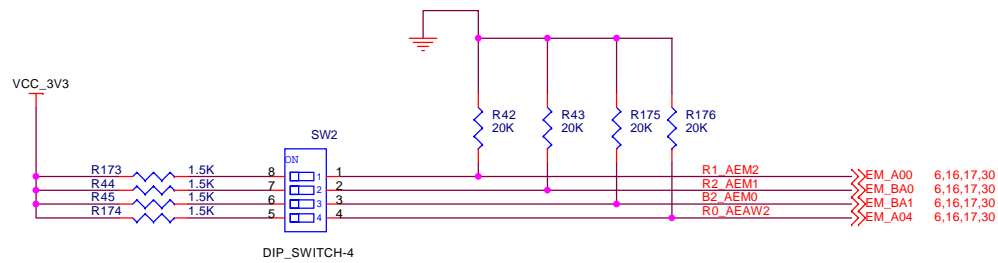
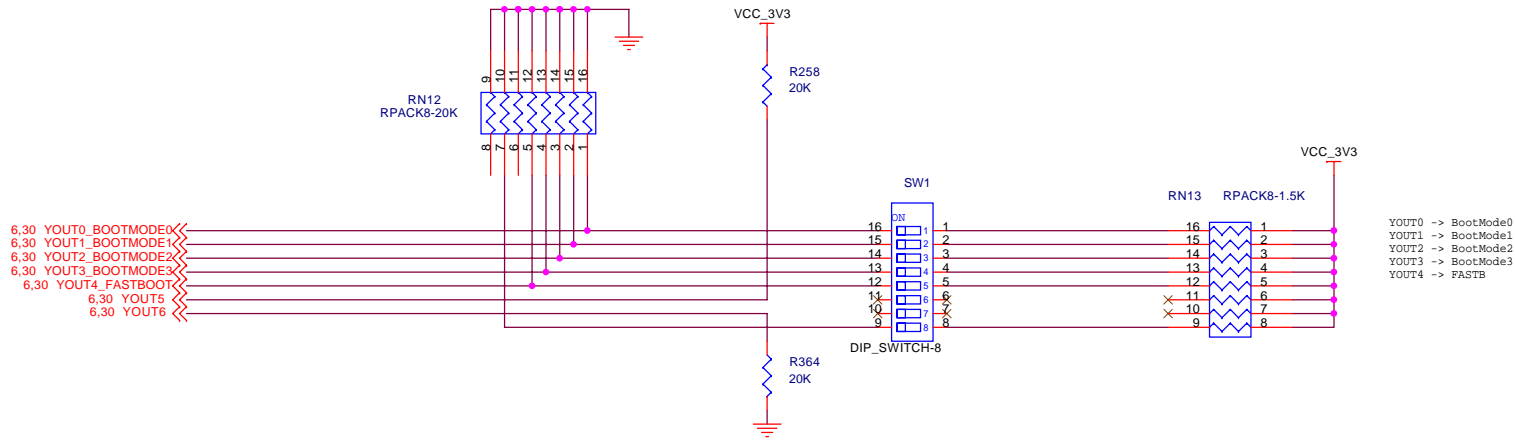
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Page Contents: DSP DDR Interface			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 7 of 34



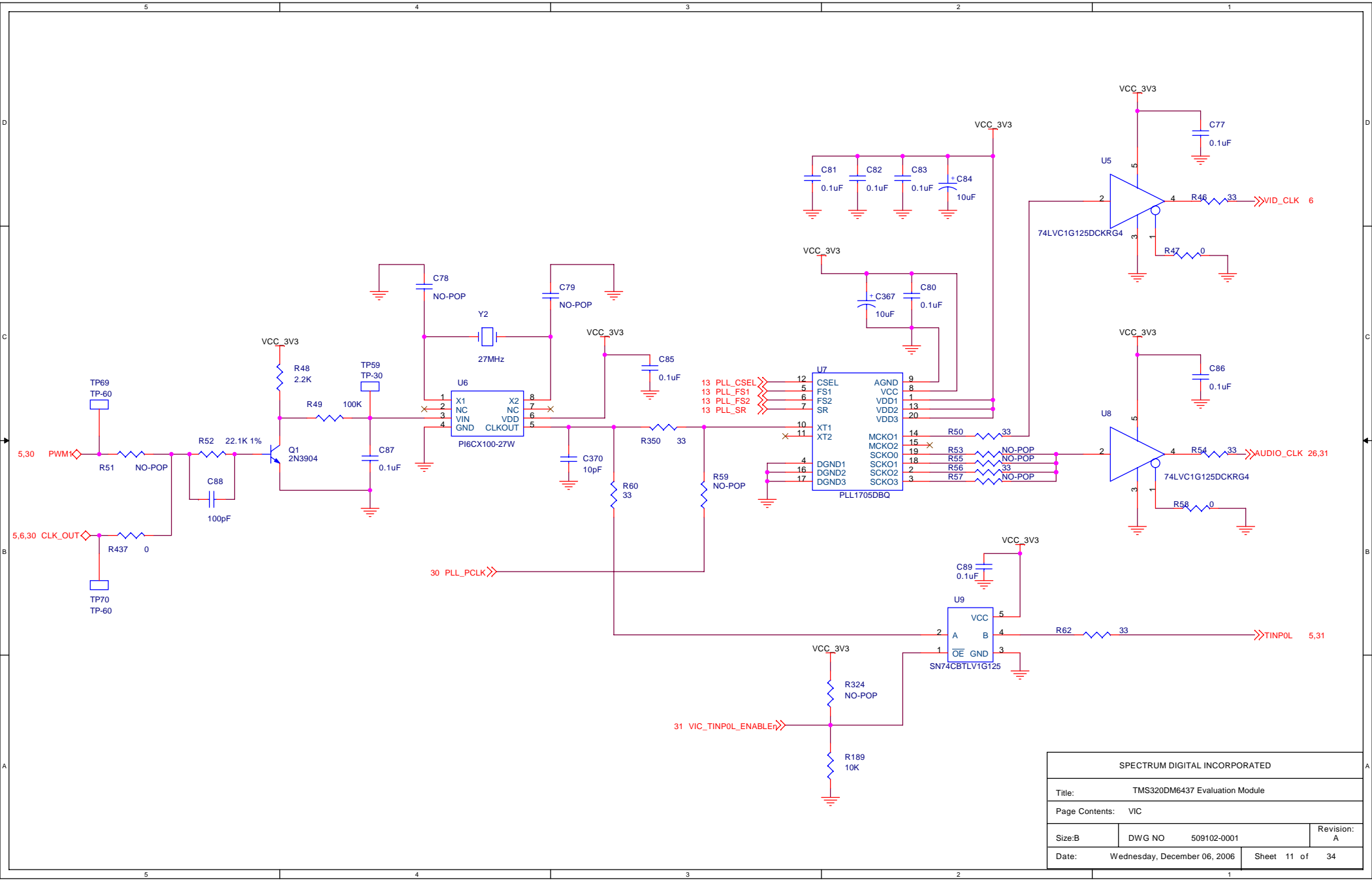
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 8 of 34



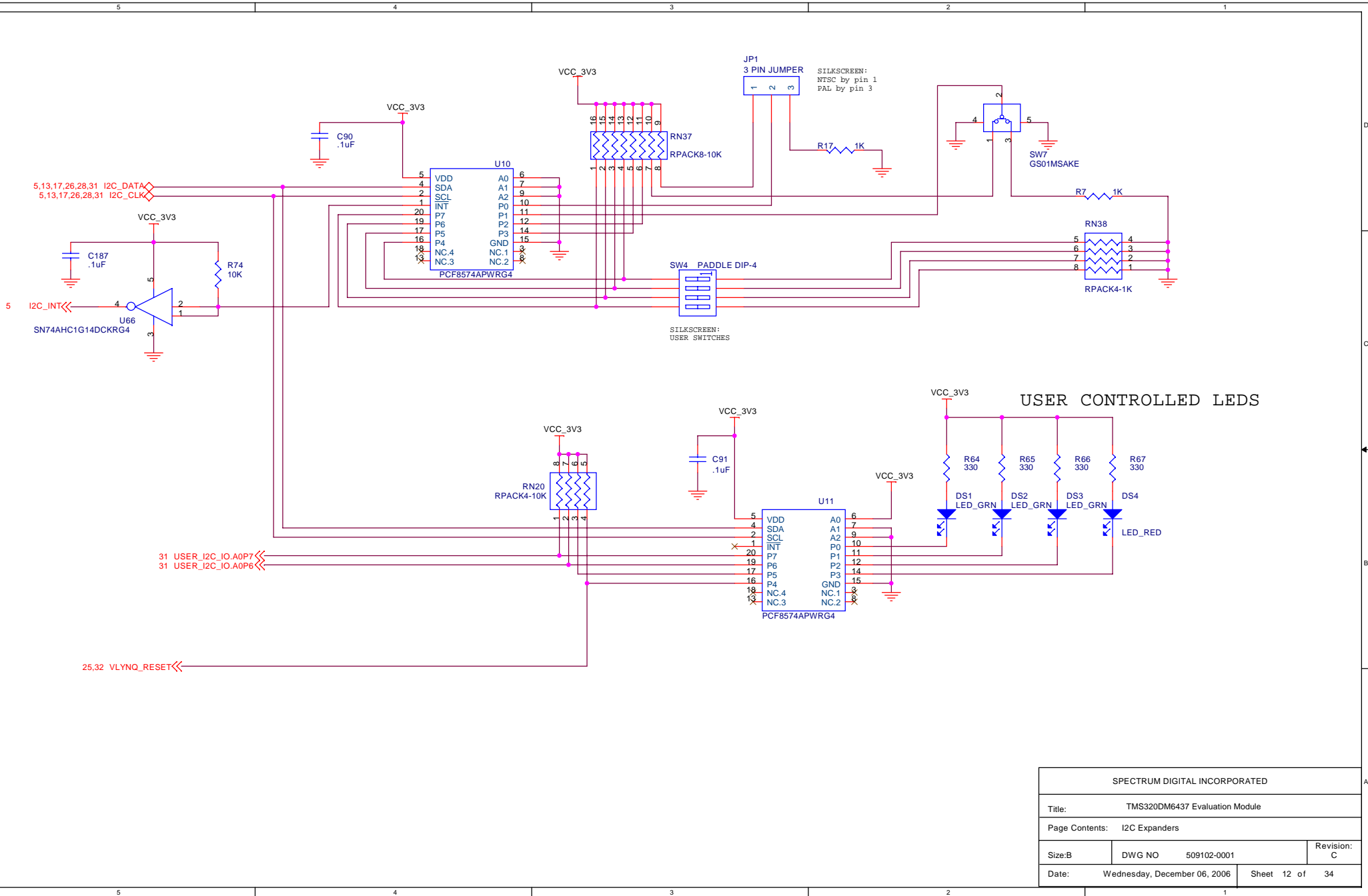
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 9 of 34	



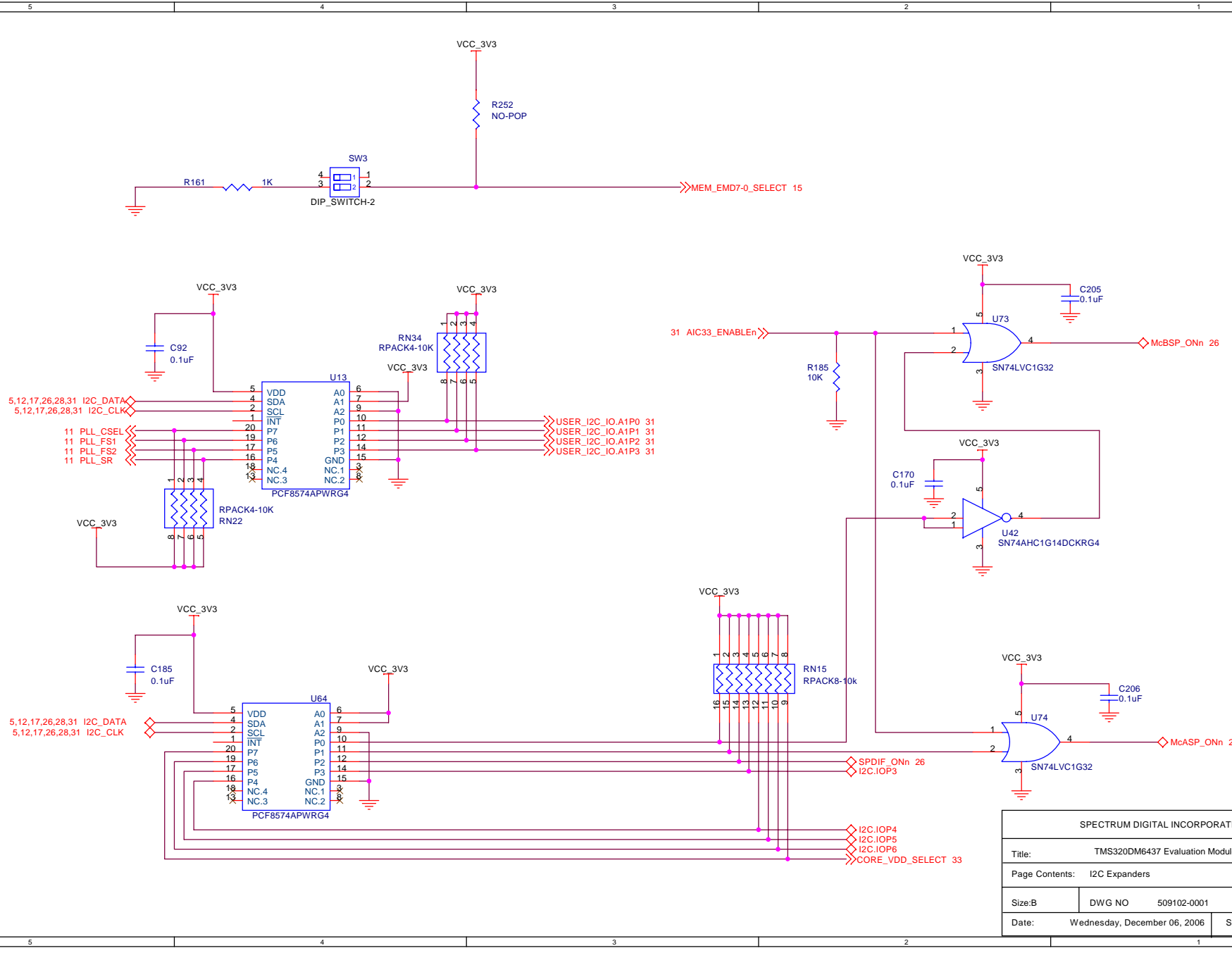
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Date: Wednesday, December 06, 2006	Sheet 10 of 34		



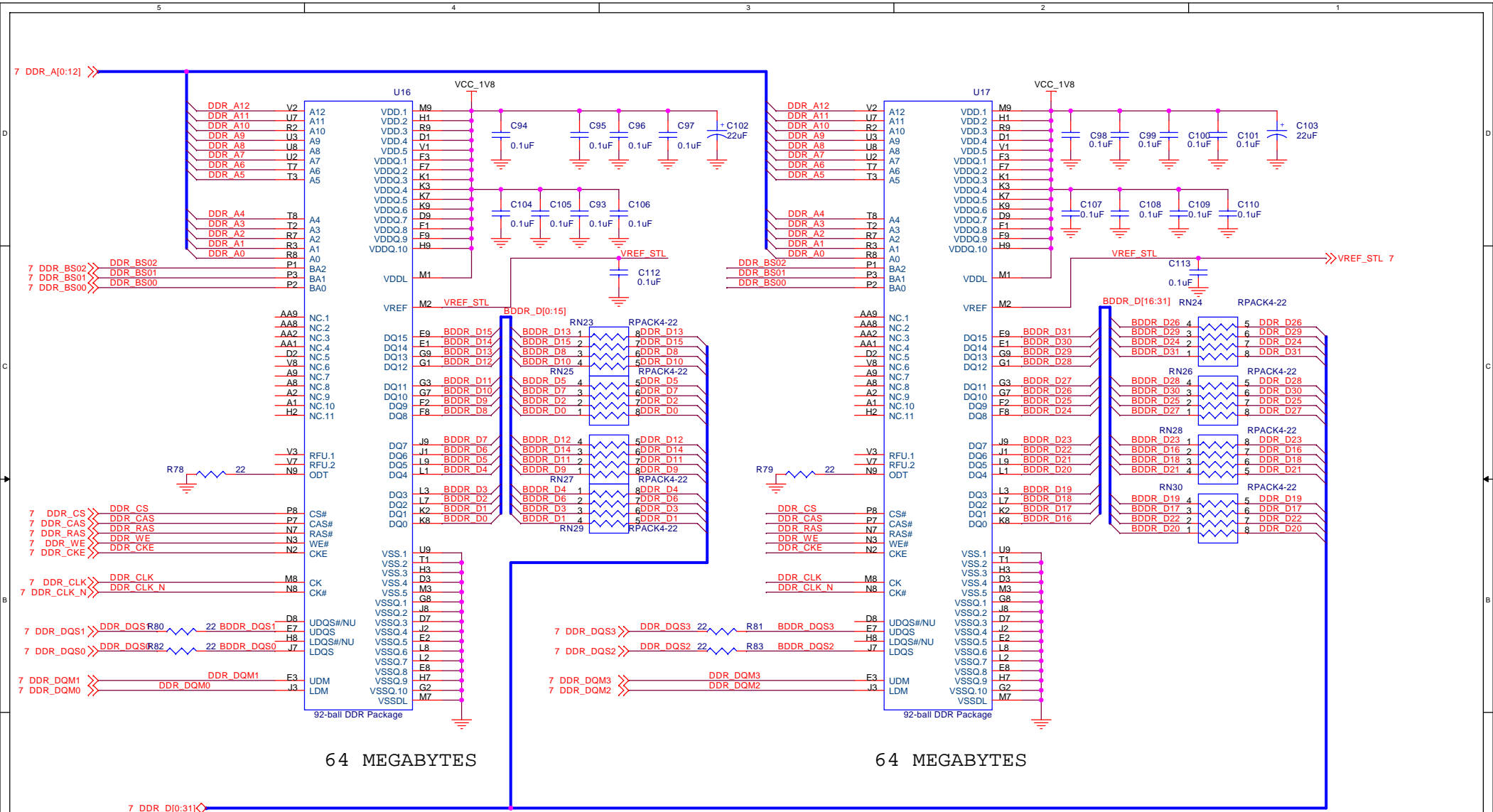
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Page Contents: VIC			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 11 of 34	



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6437 Evaluation Module			
Page Contents: I2C Expanders			
Size: B	DWG NO: 509102-0001	Revision: C	
Date: Wednesday, December 06, 2006	Sheet 12 of 34		



SPECTRUM DIGITAL INCORPORATED			
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Page Contents: I2C Expanders			
Size: B	DWG NO: 509102-0001	Revision: A	
Date: Wednesday, December 06, 2006	Sheet 13 of 34		

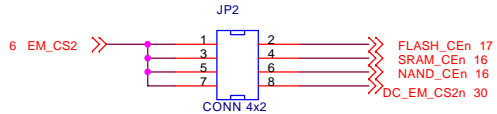
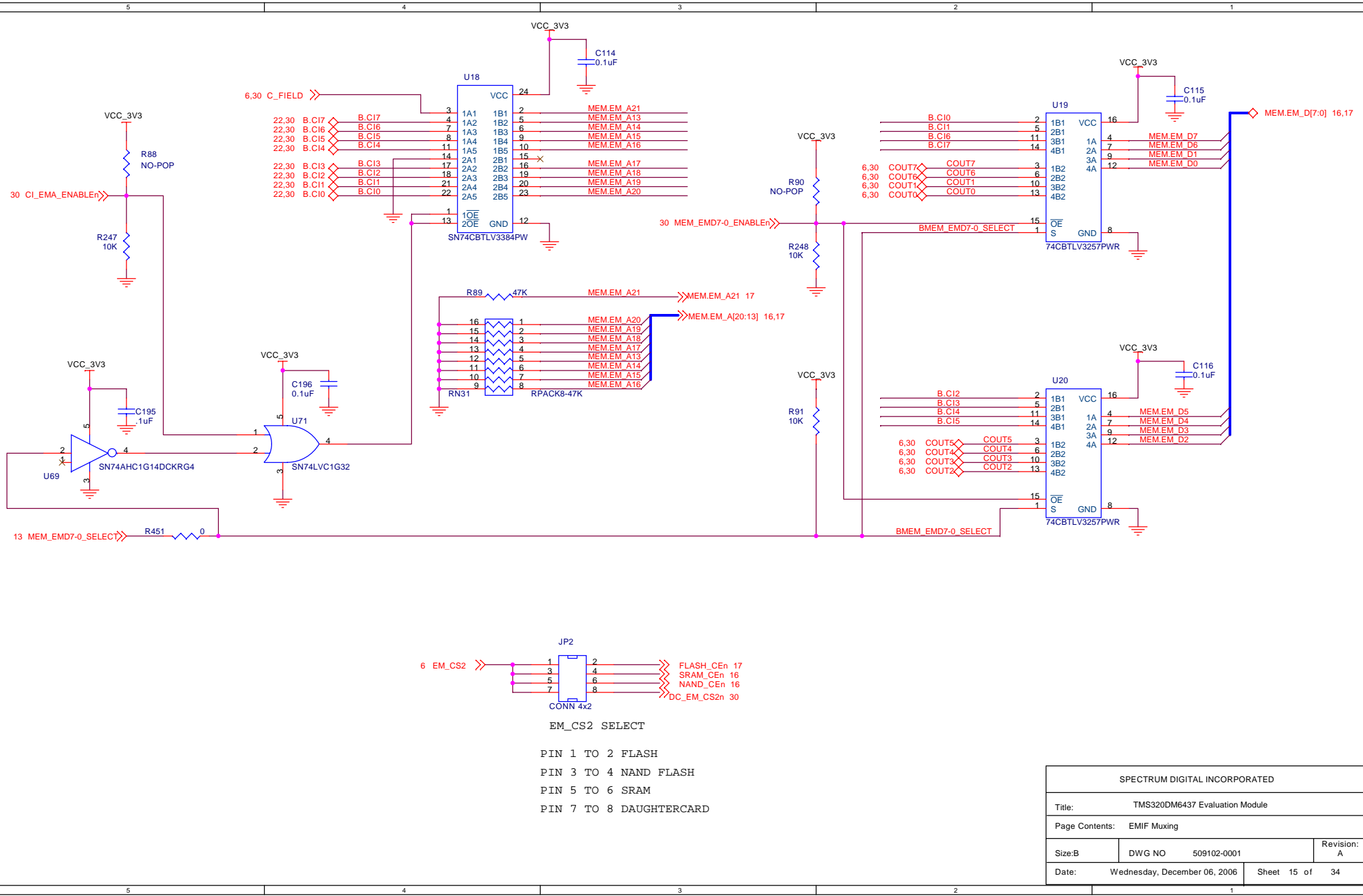


64 MEGABYTES

64 MEGABYTES

Layout schematic is shown for the 92-ball DDR Package but is compatible with 84-ball DDR2 Devices.

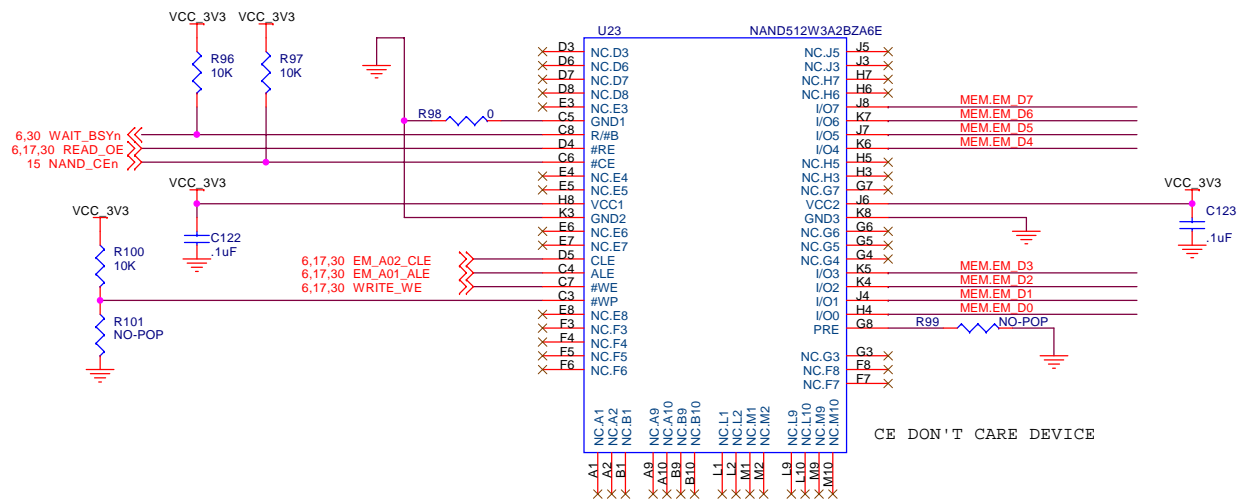
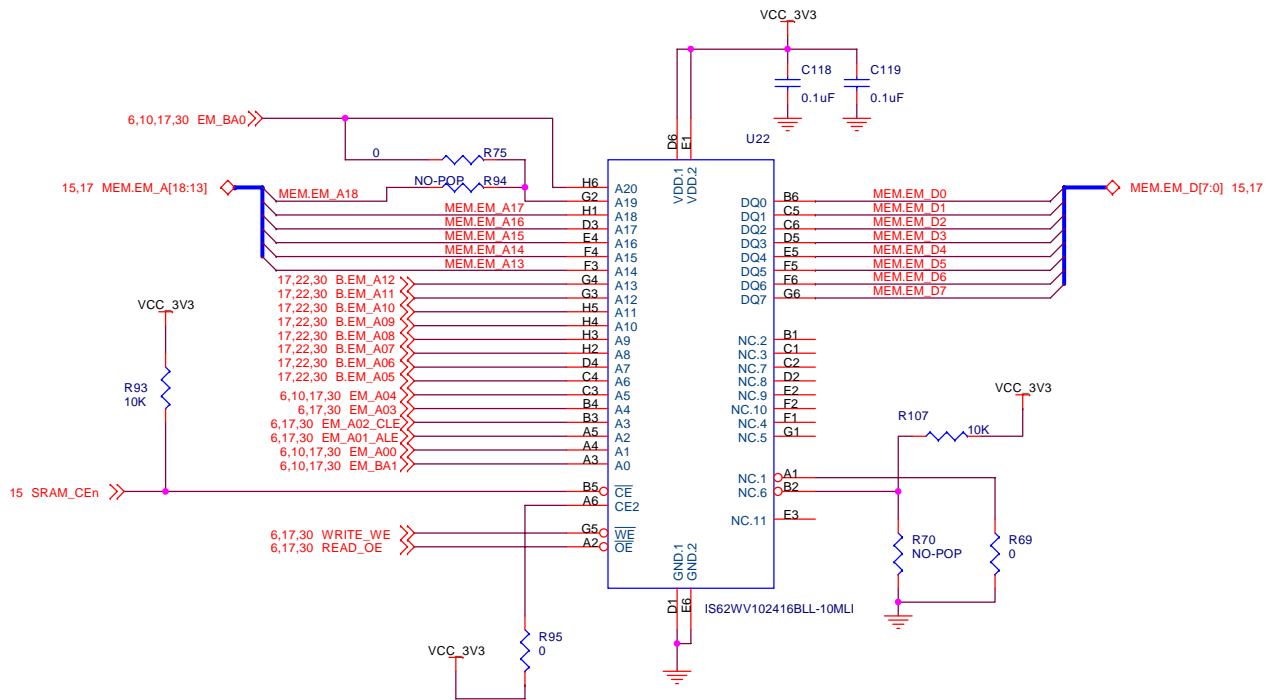
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 14 of 34



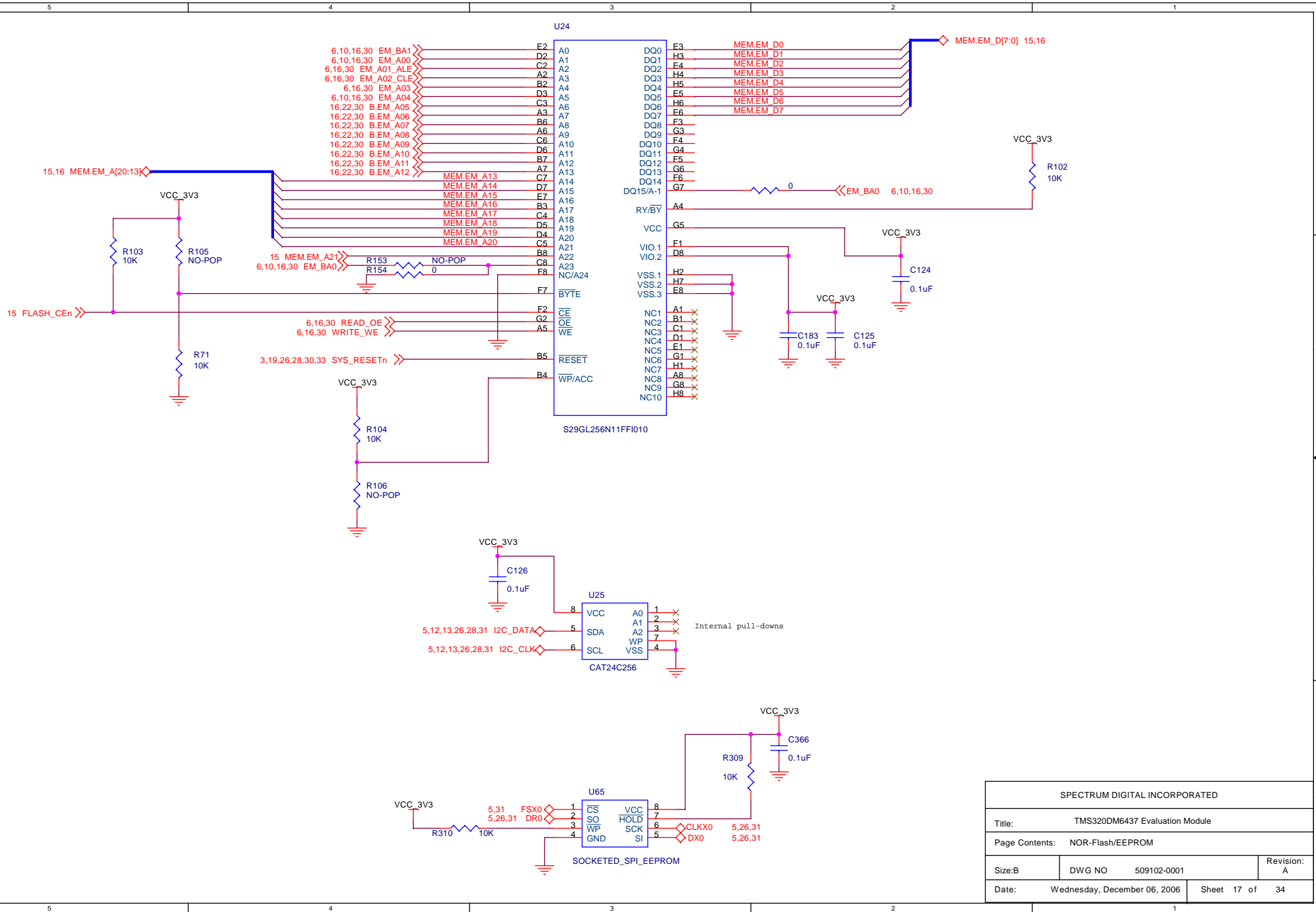
EM_CS2 SELECT

PIN 1 TO 2 FLASH
 PIN 3 TO 4 NAND FLASH
 PIN 5 TO 6 SRAM
 PIN 7 TO 8 DAUGHTERCARD

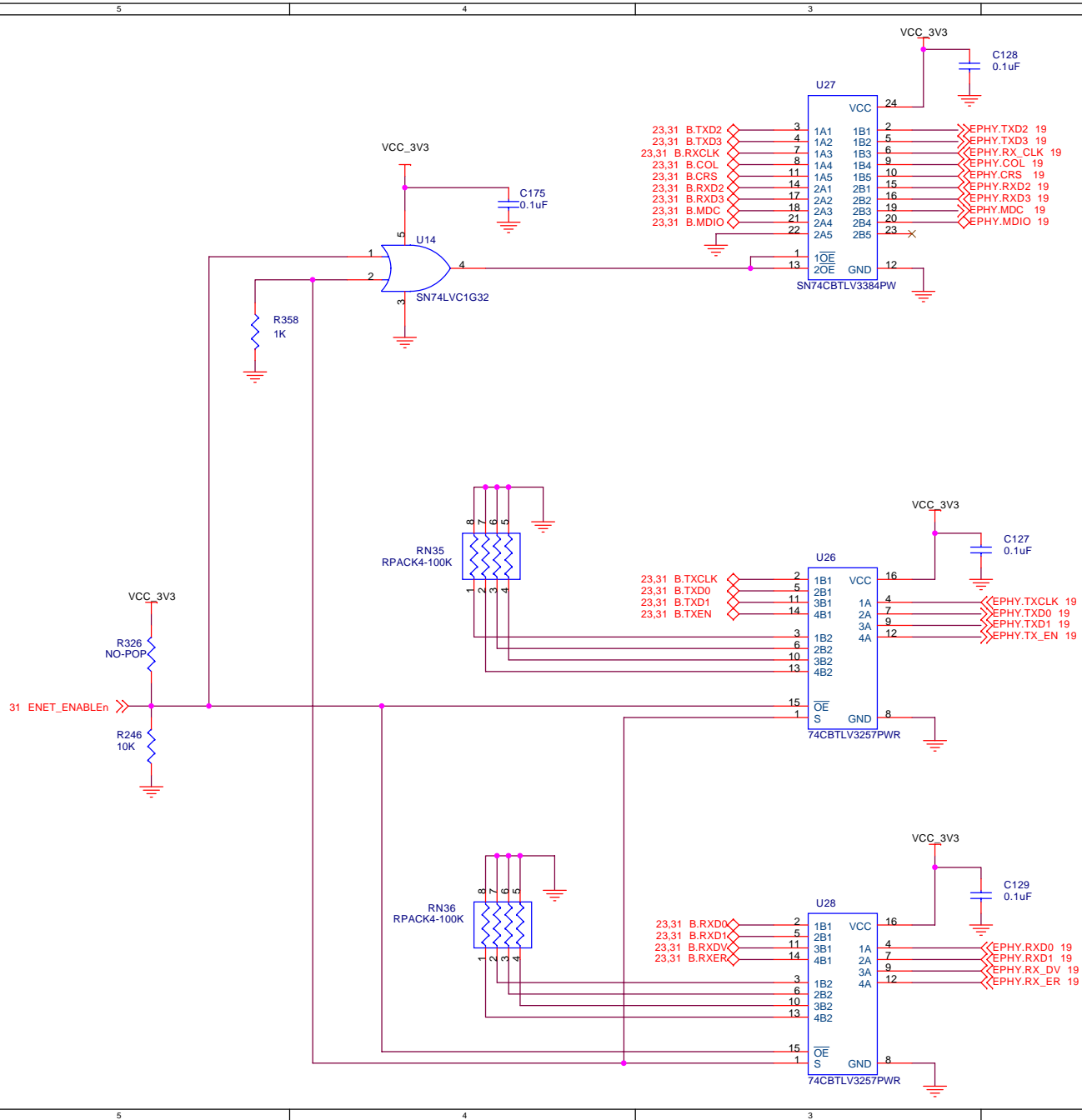
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 15 of	34



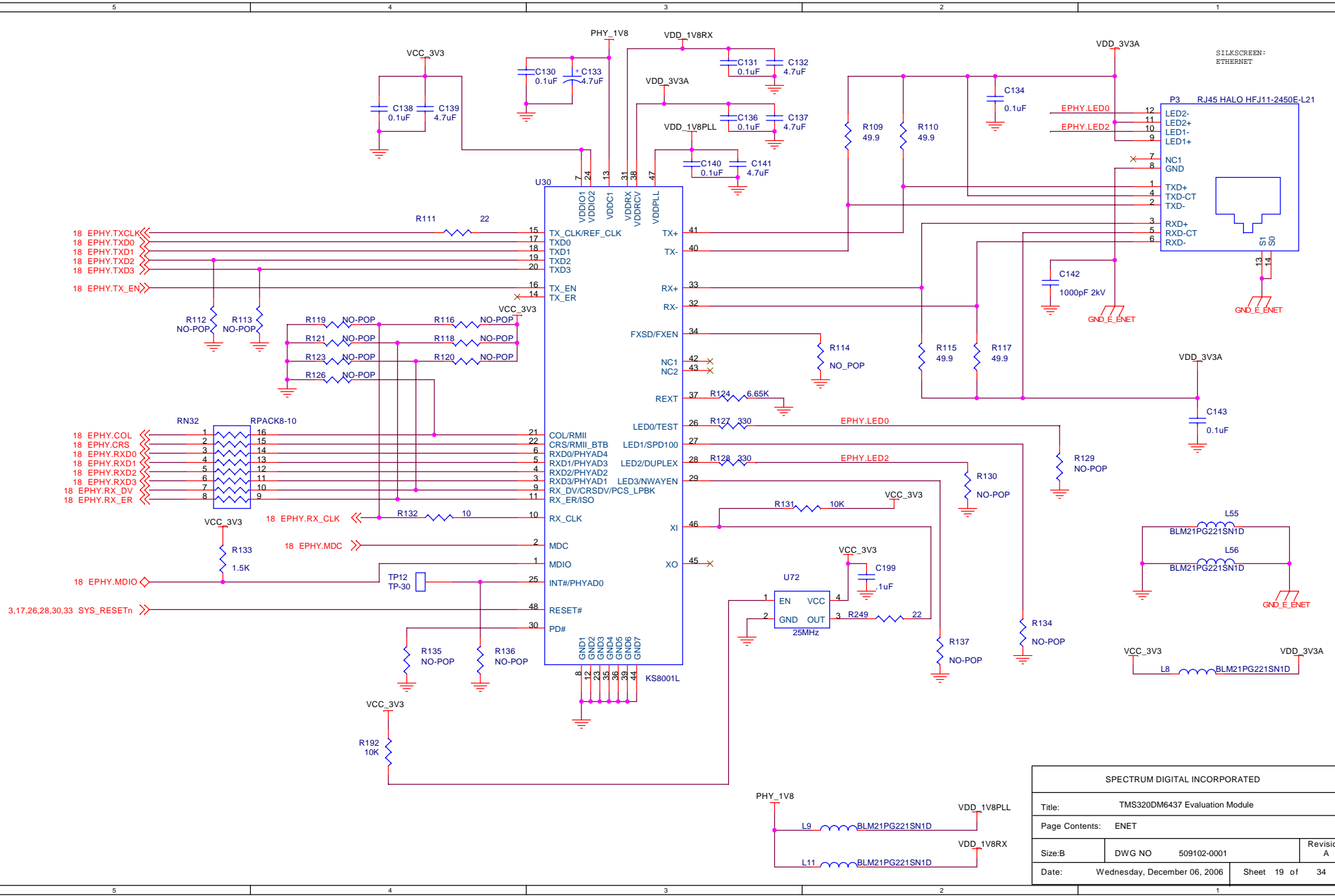
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Date: Wednesday, December 06, 2006	Sheet 16 of 34		



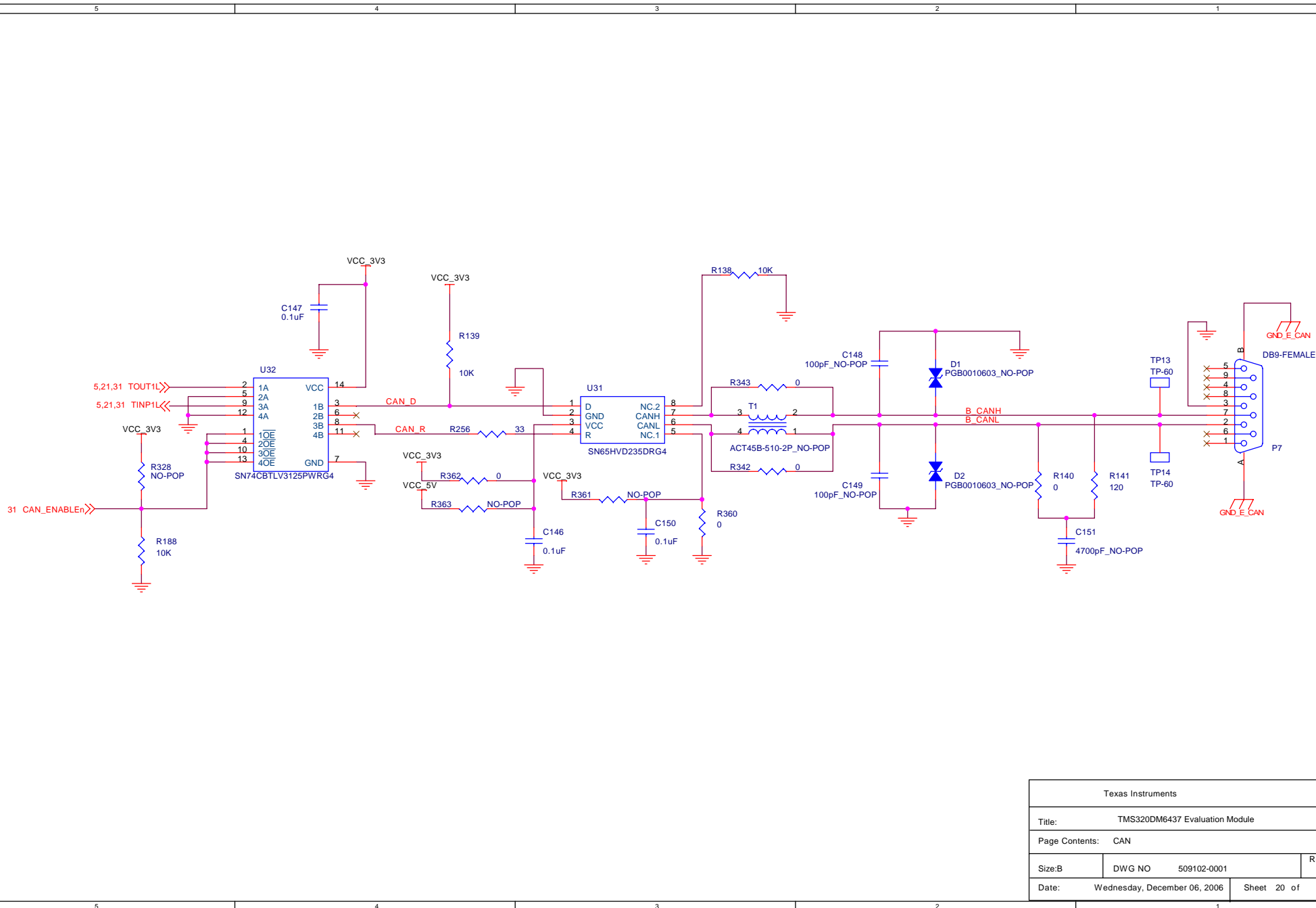
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Date: Wednesday, December 06, 2006	Sheet 17 of 34		



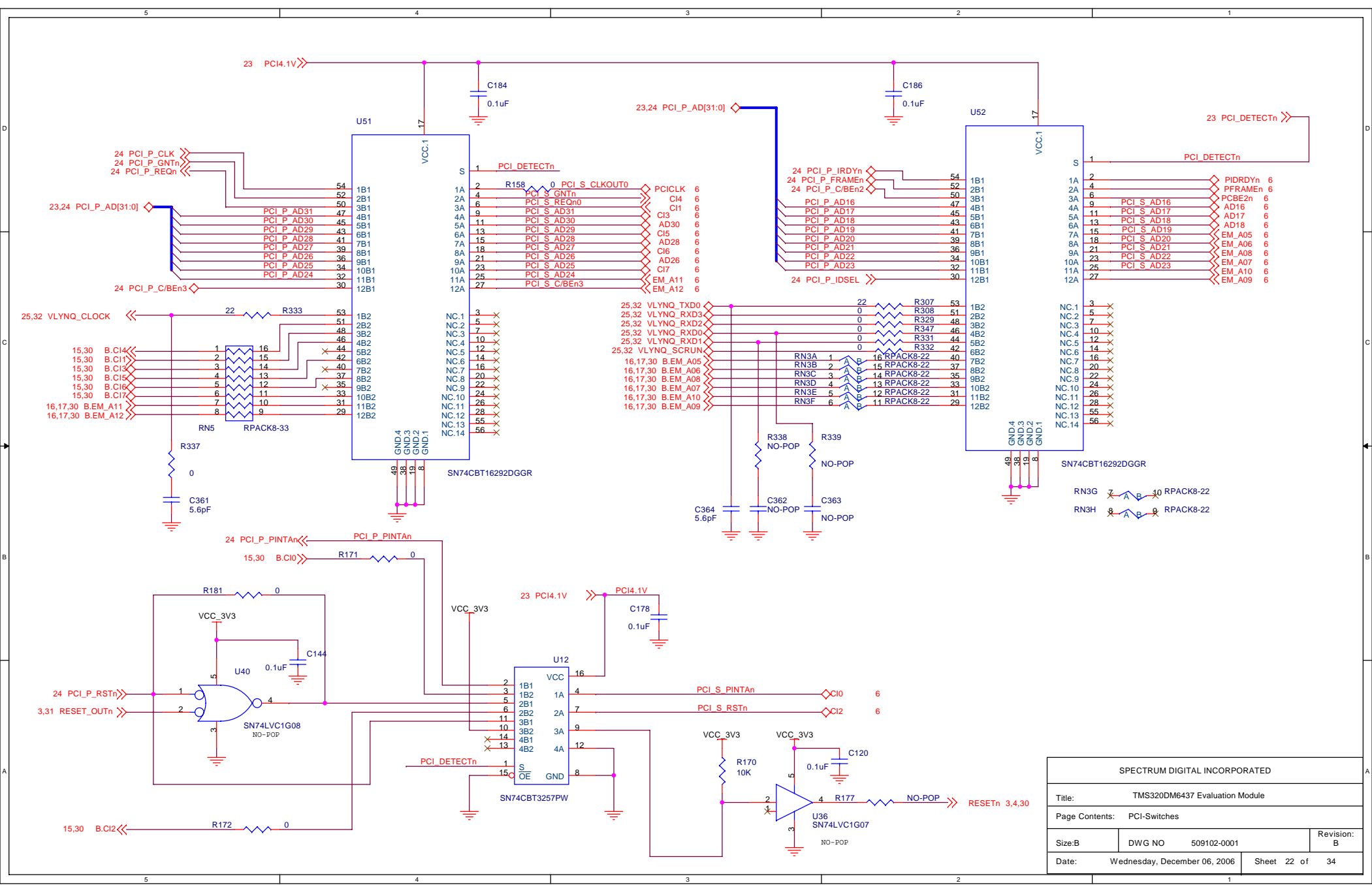
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Page Contents: ENET Muxing			
Size: B	DWG NO: 509102-0001	Revision: A	
Date: Wednesday, December 06, 2006	Sheet 18 of 34		



SPECTRUM DIGITAL INCORPORATED			
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 19 of 34

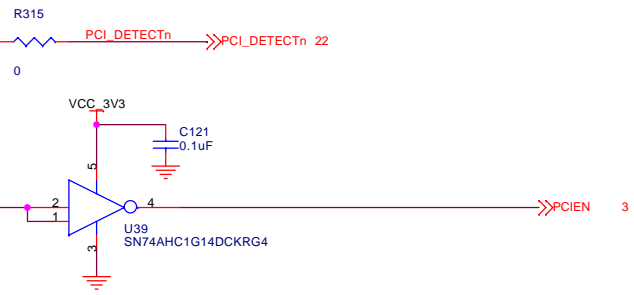
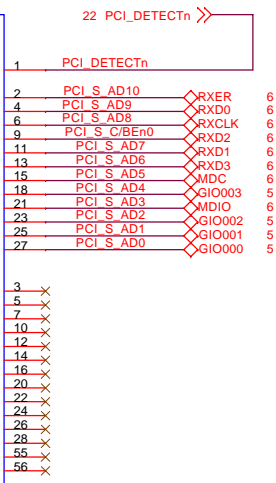
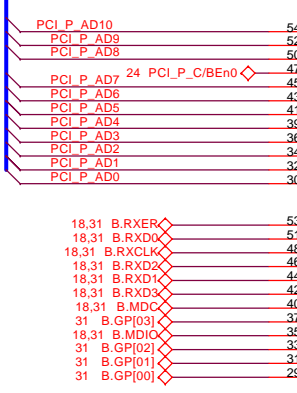
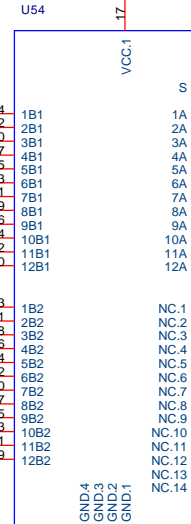
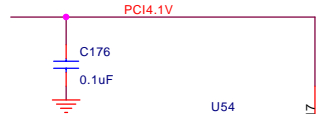
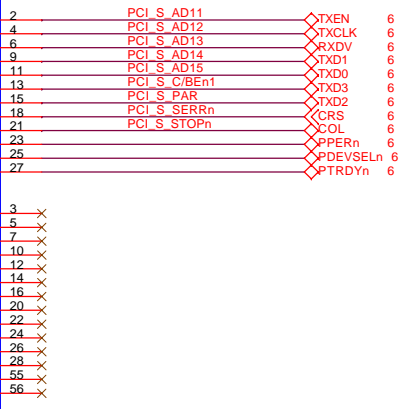
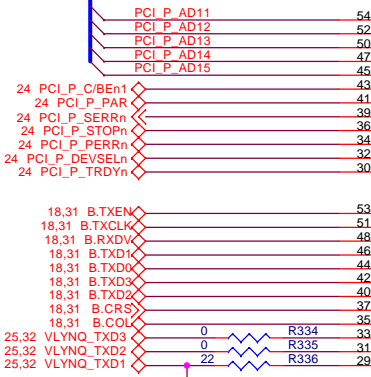
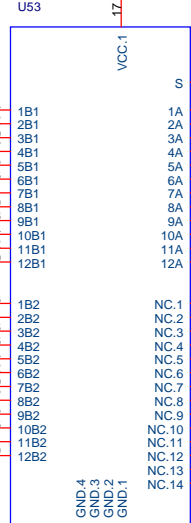
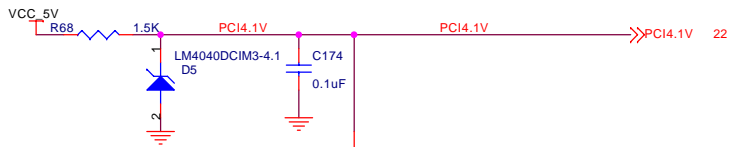


Texas Instruments			
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Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 20 of 34

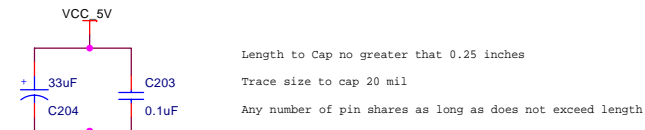
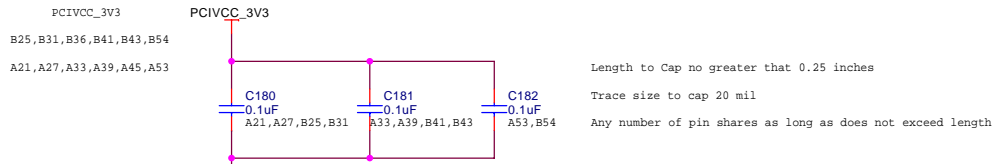
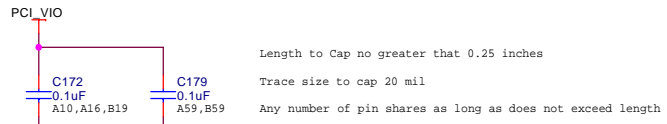
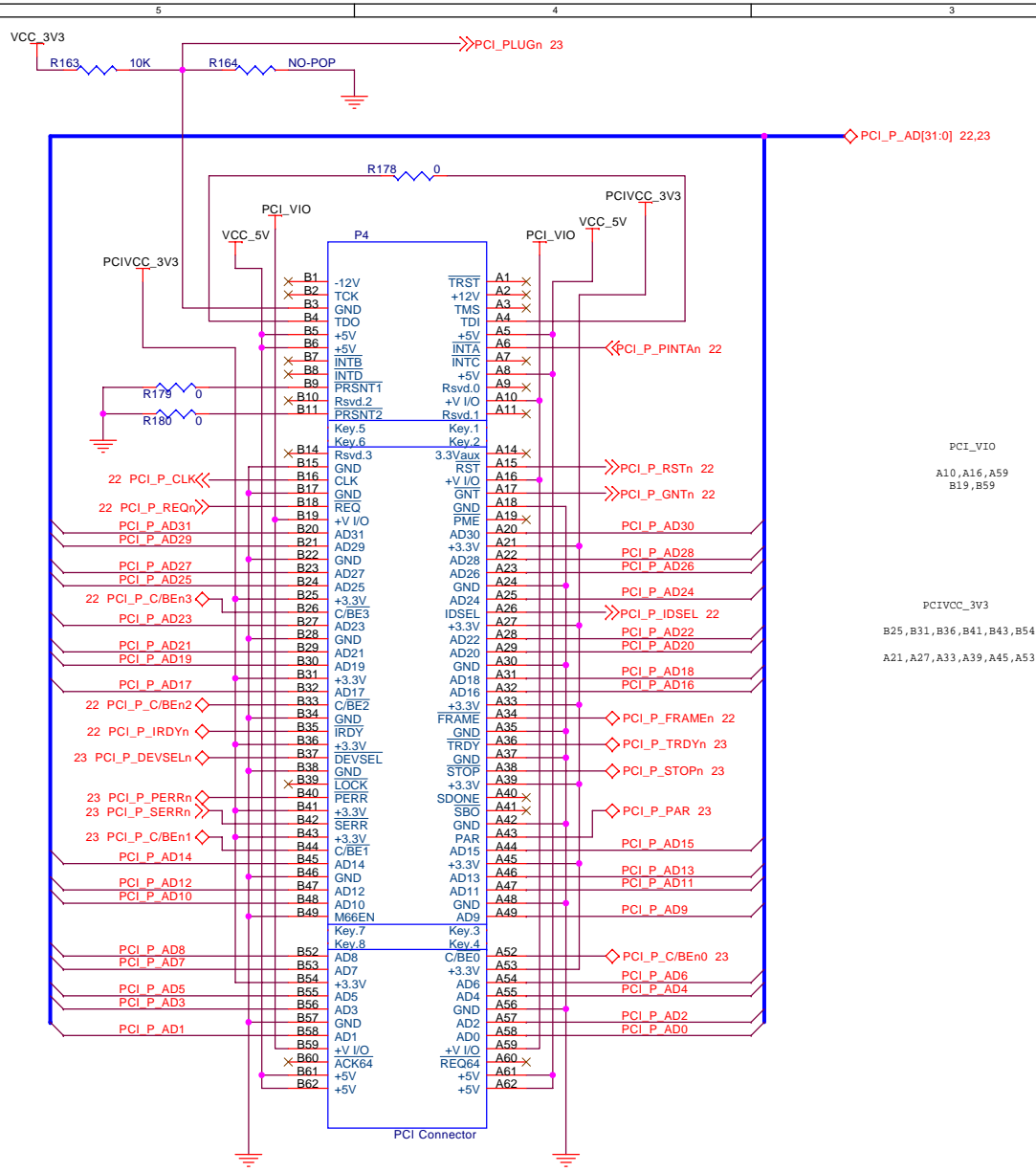


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Size: B	DWG NO	509102-0001	Revision: B
Date:	Wednesday, December 06, 2006	Sheet 22 of	34

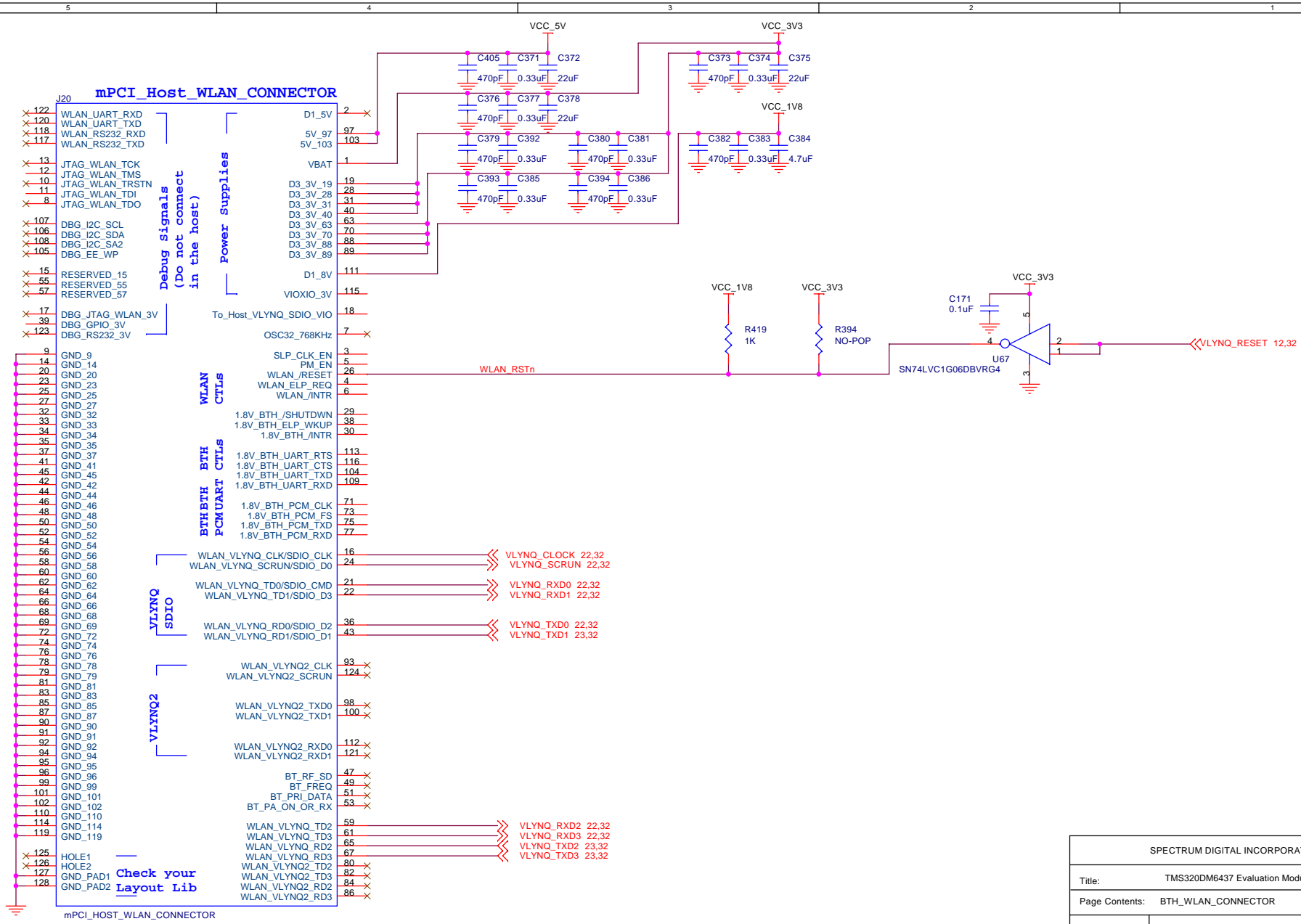
22,24 PCI_P_AD[31:0]



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Page Contents: PCI-Muxing			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 23 of 34	

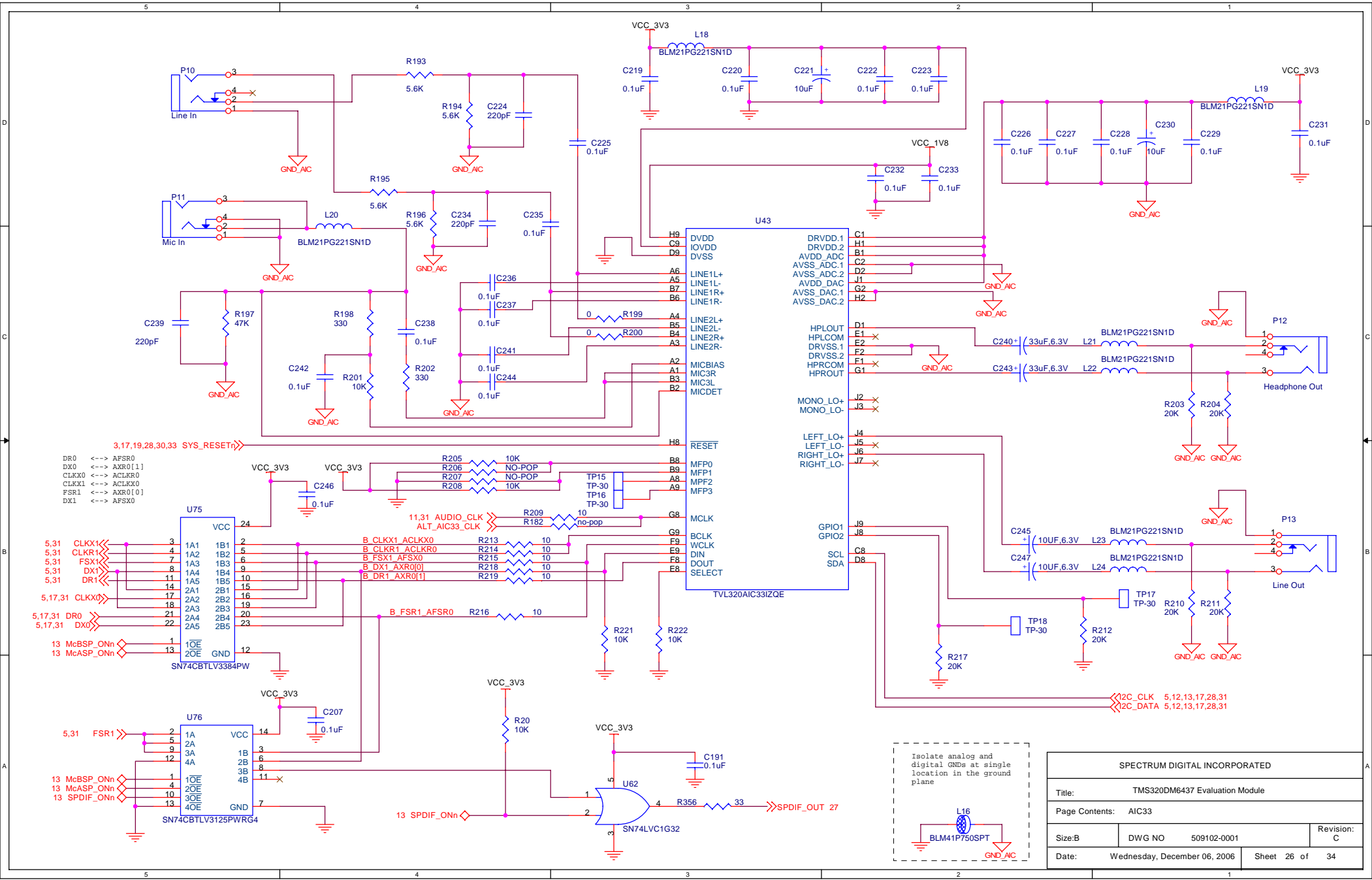


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Title: TMS320DM6437 Evaluation Module			
Page Contents: PCI-Connector			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 24 of	34



mPCI_HOST_WLAN_CONNECTOR
 SILKSCREEN: MINI PCI
 Check the layout library regarding pins 125-128.

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6437 Evaluation Module			
Page Contents: BTH_WLAN_CONNECTOR			
Size: B	DWG NO: 509102-0001	Revision: C	
Date: Wednesday, December 06, 2006	Sheet 25 of 34		



3,17,19,28,30,33 SYS_RESETn

DR0 <-> AFSR0
 DX0 <-> AXR0[1]
 CLKX0 <-> ACLKR0
 CLKX1 <-> ACLKR0
 FSR1 <-> AXR0[0]
 DX1 <-> AFSX0

5,31 CLKX1
 5,31 CLKR1
 5,31 FSX1
 5,31 DX1
 5,31 DR1

5,17,31 CLKX0
 5,17,31 DR0
 5,17,31 DX0

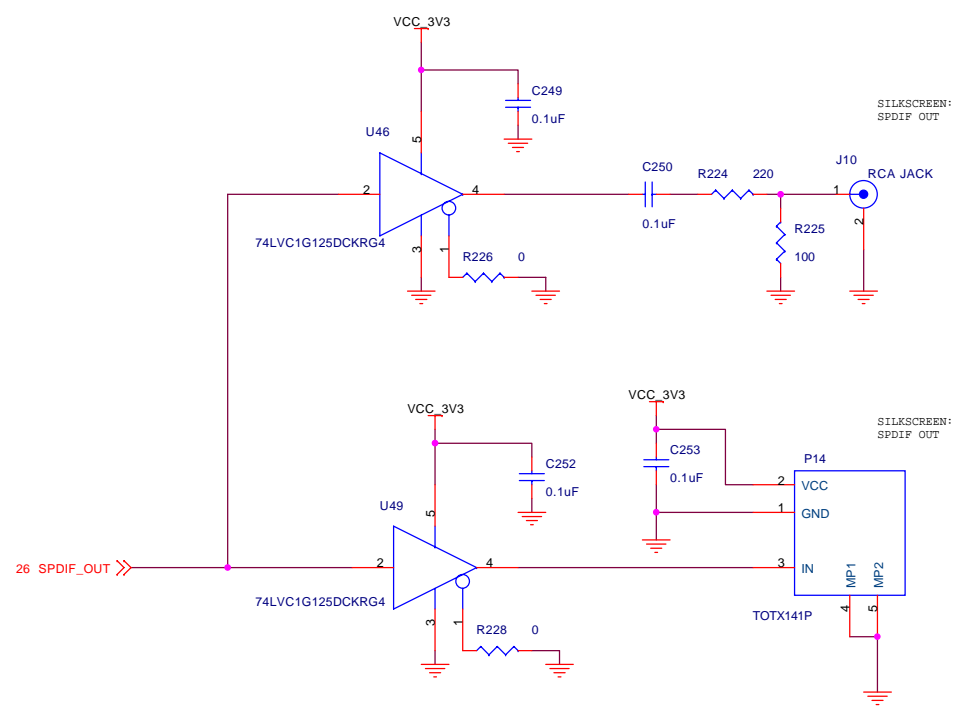
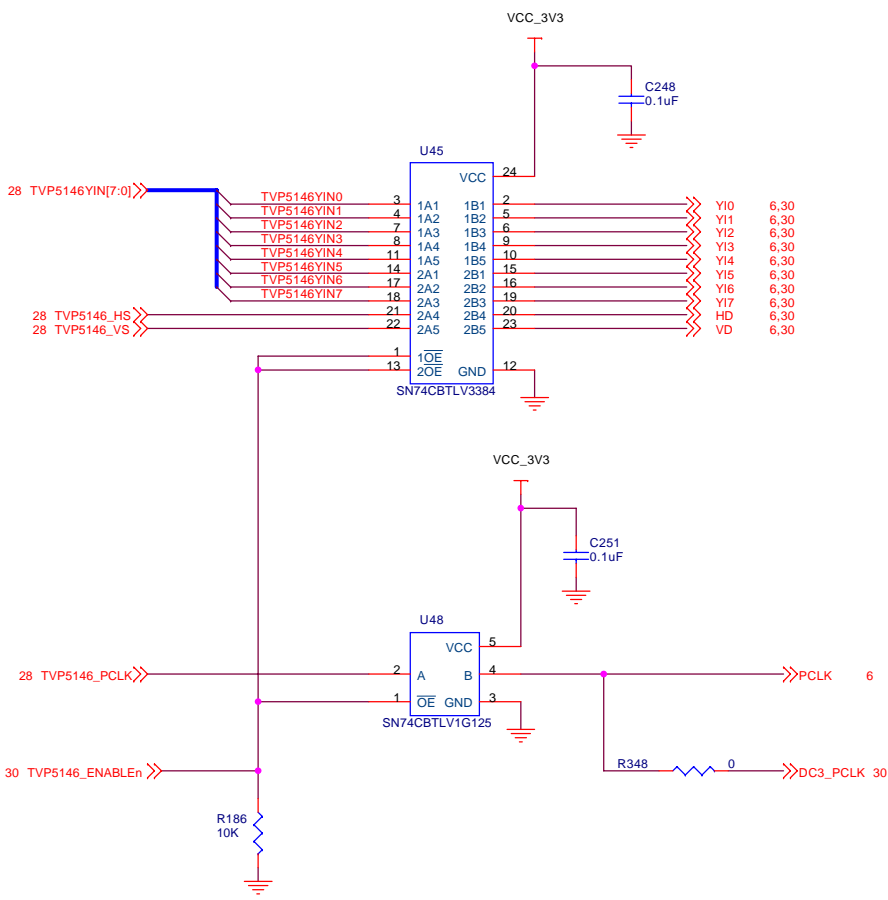
13 McBSP_Onn
 13 McASP_Onn

5,31 FSR1

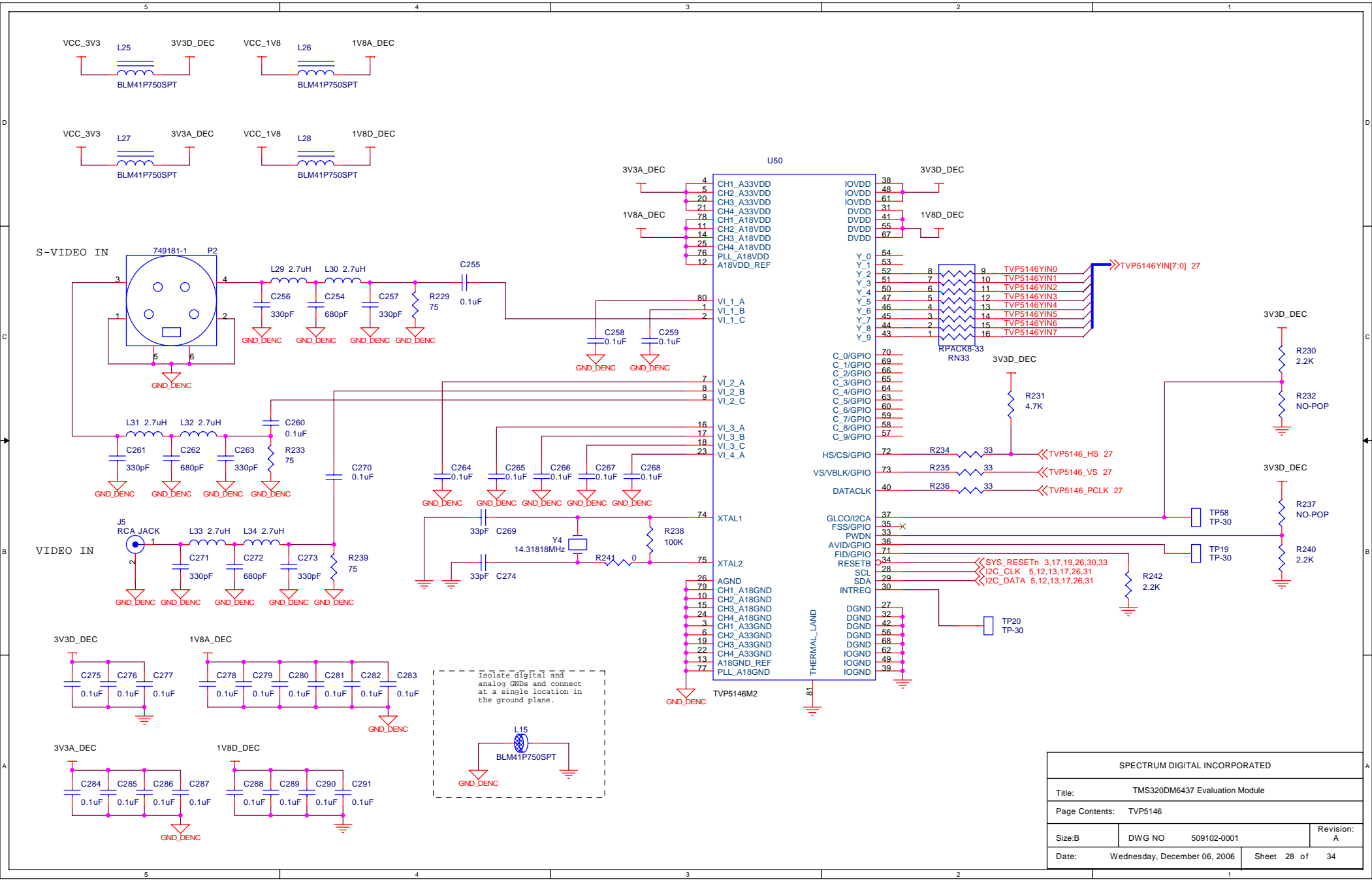
13 McBSP_Onn
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 13 SPDIF_Onn

Isolate analog and digital GNDs at single location in the ground plane

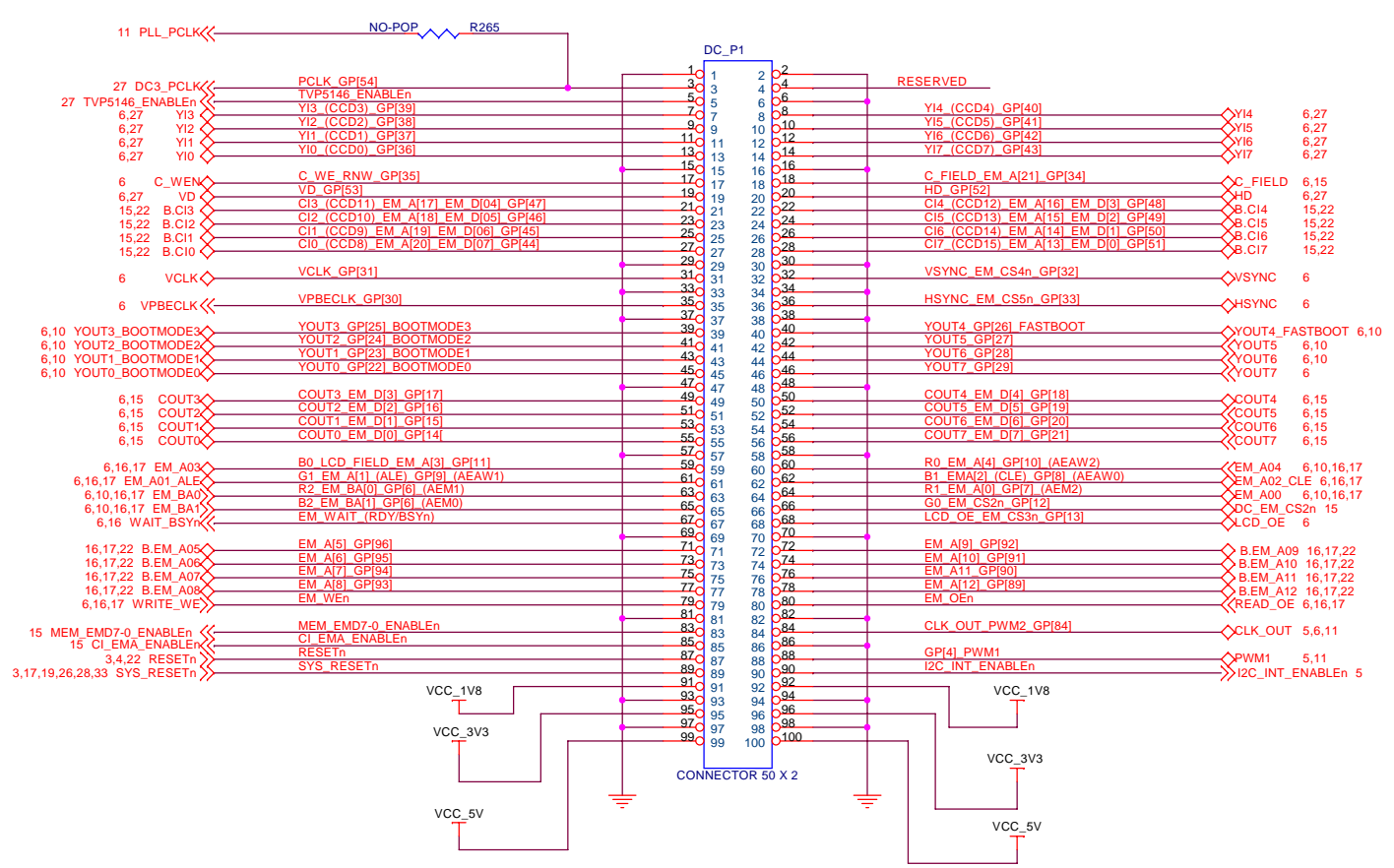
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Date:	Wednesday, December 06, 2006	Sheet	26 of 34



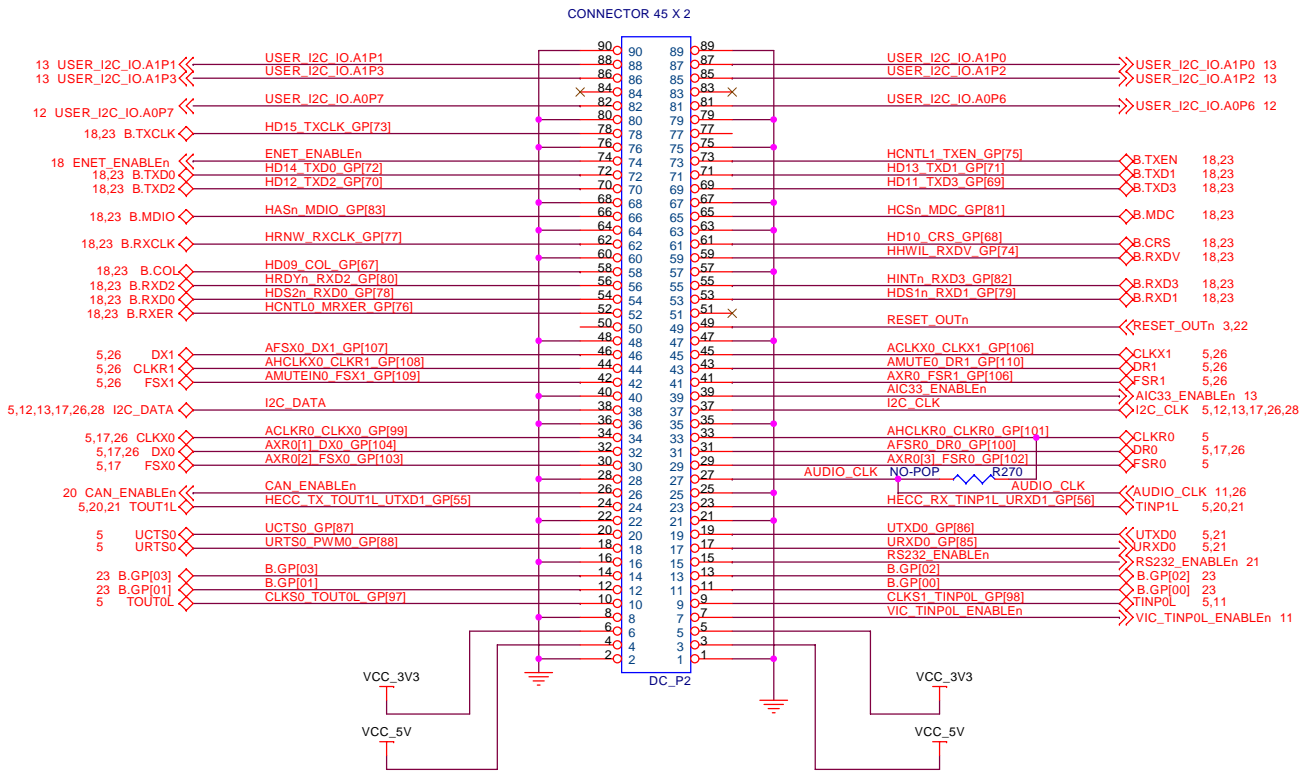
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Date:	Wednesday, December 06, 2006	Sheet 27 of	34



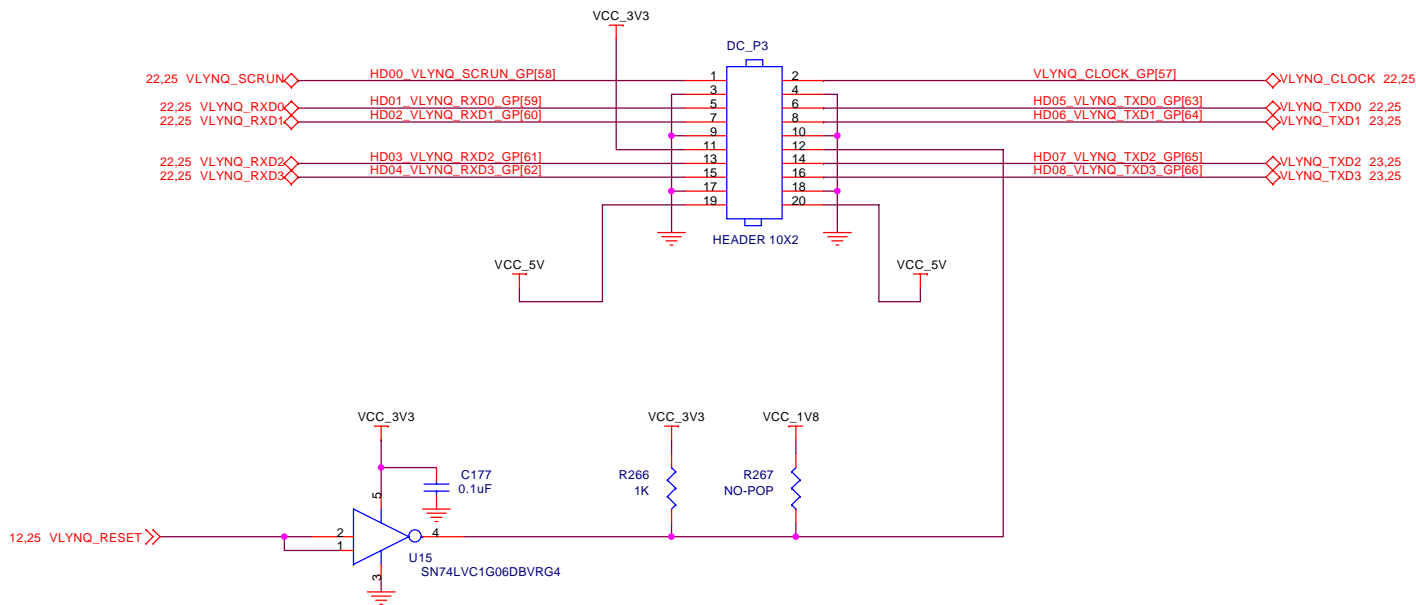
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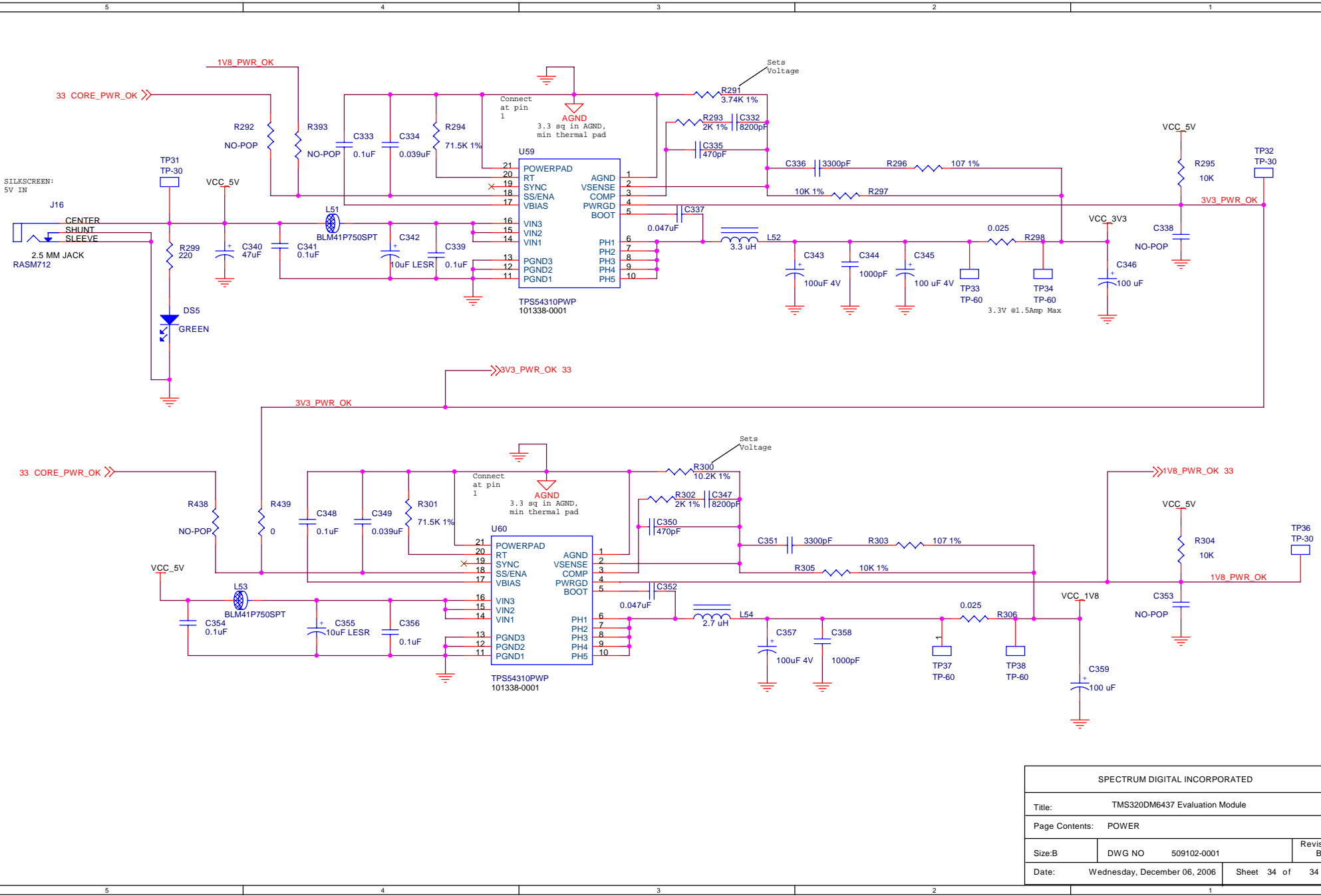
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Page Contents: VIDEO DC Conn.			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006		Sheet 30 of 34



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6437 Evaluation Module			
Page Contents: EMAC/MCBSP DC Conn.			
Size: B	DWG NO	509102-0001	Revision: A
Date: Wednesday, December 06, 2006			Sheet 31 of 34



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6437 Evaluation Module			
Page Contents: VLYNQ/EMIF DC Conn.			
Size: B	DWG NO	509102-0001	Revision: A
Date:	Wednesday, December 06, 2006	Sheet 32 of	34



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6437 Evaluation Module			
Page Contents: POWER			
Size: B	DWG NO	509102-0001	Revision: B
Date:	Wednesday, December 06, 2006	Sheet 34 of 34	